



**Service Mode Cont'd**

**NOTE 2:** The Factory Mode flag is set to '1' when the T.V. is in Factory Mode. Factory mode can be entered by pressing both VOLUME +/- buttons on the front of the set whilst it is being powered up.

**NOTE 3:** Auto Tune can be enabled so when the TV. is switched on, it will start tuning in the TV. (after the user has gone through the INSTALLATION MENU). This flag is enabled when the user presses the STANDBY button or switches the set off when the AUTOMATIC TUNING screen is displayed.

**NOTE 4:** The I Standard VHF disable flag informs the TV. whether or not it should begin tuning at 441.10 MHz or 41.10 MHz. If this is set then tuning will begin at 41.10 MHz. If this is '0' and the single standard flag is '1', then tuning will begin at 441.10 MHz instead.

**NOTE 5:** For the A5 75 Model Range of T.V.'s, this flag will be set to indicate to the software that no Dolby board is present.

**NOTE 6:** For sets that should have TOP TEXT available, the TOP TEXT ENABLE flag should be set to '1'.

**NOTE 7:** For a single standard chassis (CP\*\*\*TA, CP\*\*\*TAN, and C\*\*\*TN) this flag should be set to '1'.

**NOTE 8:** For AS 25" and 28" models, the COMB FILTER will not be fitted and as such this flag should be cleared to '0'.

**The GEOMETRY, GREY SCALE, A2, and SERVICE BYTE values can be stored by pressing the MENU button whilst the SERVICE MODE screen is displayed. If the user makes any mistake, the T.V. ( ) button can be pressed to clear the screen. The "ESC" button can then be pressed to re-enter SERVICE MODE again.** □

**A5 CHASSIS ALIGNMENT PROCEDURE****1. APPLICATIONS****2. P. W.B. ASSEMBLY ADJUSTMENT****2.1 FOR SIGNAL****2.1.1 PREPARATION ADJUSTMENT****2.1.2 AGC ADJUSTMENT****2.1.3 AFC ADJUSTMENT****2.2 FOR POWER AND DEFLECTION****2.2.1 +B VOLTAGE ADJUSTMENT****2.2.2 POWER GOOD LINE****2.2.3 AUDIO FEEDBACK TRIM****2.2.4 HIGH VOLTAGE LIMITER CIRCUIT CHECK****2.2.5 ANODE/FOCUS SHORT-CIRCUIT TEST PROTECTION CIRCUIT****2.2.6 SUB PSU +9.5 V VOLTAGE SET****2.2.7 BASIC TEST SPECIFICATION****2.2.8 PRIMARY CURRENT LIMIT (C28300 ONLY)****3. FINAL ALIGNMENT (BY SOFTWARE ADJUSTMENT)****3.1 PICTURE POSITION/SHAPE****3.1.1 HORIZONTAL VERTICAL CENTRE VERTICAL AMPLITUDE****3.1.2 TILT PARABOLA WIDTH****3.2 FOCUS ADJUSTMENT****3.3 SCREEN CONTROL/CUT OFF ADJUSTMENT****3.4 WHITE BALANCE ADJUSTMENT****4. PIN P ADJUSTMENT****5. ADJUSTMENT POINTS****6. SERVICE BYTE ASSIGNMENT INFORMATION****1. APPLICATIONS**

THIS SPEC. SHOULD BE APPLIED TO C2576/77TN AND C2876/77TN (28300)

**2. RW.B ASSEMBLY ADJUSTMENT****2.1 FOR SIGNAL****2.1.1 PREPARATION ADJUSTMENT**

- 1) +B adj. VR950 .... Centre
- 2) Screen VR (FBT) .... Counter-clockwise fully
- 3) Turn on set. Adjust +B to approximately 150V. (Pre adjustment only - full adjustment in section 2.2.1)

**2.1.2 AGC ALIGNMENT**

- 1) With the signal received, apply heat run for more than two minutes to avoid the influence of circuit temperature drift.
- 2) Connect a voltmeter at least 100K internal resistance to the A.G.C. terminal of the tuner.
- 3) Receive the channel below.
- 4) Adjust A.G.C. potentiometer VR201 until the following voltage is obtained.

**2.1.3 AFC ALIGNMENT****L202 ALIGNMENT**

- 1) apply relevant RF signal. Circle Pattern
- 2) on dual/multistandard receivers select BG standard.
- 3) Enter frequency on CTV controls to ensure AFC loop is off.
- 4) Connect oscilloscope and voltmeter to pin 23 IC201 (TDA9815) or pin 13 (TDA9800).
- 5) Adjust L202 until fast rate of change is seen on the oscilloscope.
- 6) Adjust L202 so that the voltmeter reads 2.5V 0.2V.

**VR202 Alignment (CL\*\*76/77TAN only)**

- 1) Apply L standard RF signal.
- 2) Select L standard on CTV controls (system L VHF band 1).
- 3) Enter frequency on CTV controls.
- 4) Connect oscilloscope and voltmeter to pin 23 IC201.
- 5) Adjust VR202 until a fast rate of change is seen on the oscilloscope.
- 6) Adjust VR202 so that the voltmeter reads 2.5V ± 0.2V.

**2.2 POWER AND DEFLECTION ADJUSTMENT****2.2.1 +B VOLTAGE ADJUSTMENT**

- 1) AC input voltage = 230V + SV/5Hz.
- 2) Turn +B voltage VR (VR950) to mid-point (if pre-adjustment not done).
- 3) Receive Philips circuit pattern. Switch on chassis and set the brightness and contrast to maximum.
- 4) After applying heat run for 30 sec. or more, turn VR950 gradually and adjust +B (re-check after 2 minutes heat run). Measuring point: +B voltage C955 + side gnd C955 - side
- 5) Set the value of +B voltage to the value shown in the following table.

**Model**

C2577      +B Voltage (V)  
150 ± 0.2V

C2576      150 ± 0.2V

C2877      150 ± 0.2V

C2876      150 ± 0.2V

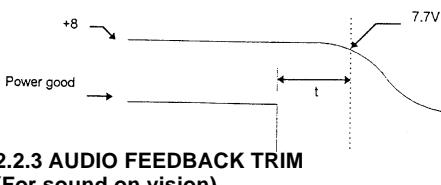
C28300TN

6) Short circuit test (all rails). PSU should go into standby/reset/lockup. (Supply may have to be removed to restart)

7) Standby check. +B should go to 120V < +B >140V. ±8, +5, +12V should be 0V.

**2.2.2 POWER GOOD LINE**

- 1) Set picture to same conditions as above.
- 2) Measure pin 1 PL951. Should be HI, if LOW then cut R945 (if fitted). If HI but no power down timing (see below) then cut R991.
- 3) Check power down logic timing (>5ms).

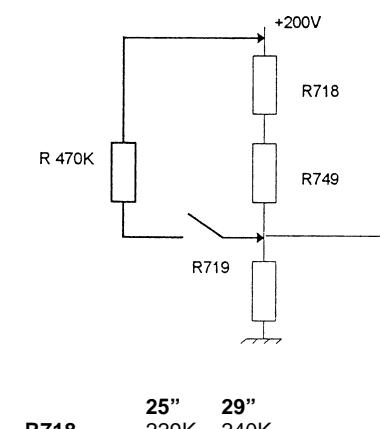
**2.2.3 AUDIO FEEDBACK TRIM (For sound on vision)**

If sound on vision is seen with maximum bass 1/2 volume on normal Picture - STEREO Sound then cut trim resistor R969A (to change R969 value to 22K)

**2.2.4 HIGH VOLTAGE LIMITER CIRCUIT CHECK**

- 1) Mount the PW board to the set and adjust normally.
- 2) Receive the circle pattern signal.
- 3) Set the contrast and brightness to maximum.
- 4) Add R=470K in parallel with R718 and R749.
- 5) Check that picture and sound disappear when R is added.

**NOTE:** High voltage limiter circuit jig:



To comparison circuit 10953 (Protection)

**2.2.5 ANODE/FOCUS SHORT-CIRCUIT TEST PROTECTION CIRCUIT CHECK**

- 1) Receive the circle pattern signal.
- 2) Set the contrast/brightness to maximum.
- 3) Check trip point by adding an external D.C. supply across R730. The set should not trip when a 0.8V supply is added. The set should trip when a 1.2V supply is added.

**2.2.6 SUB POWER SUPPLY ADJUSTMENT**

- 1) Switch on power supply (mains input 230V ± 5V 50Hz).

2) Measure +9.5V output. If output is greater than 9.8V then cut R9107 (220K 1/4W).

3) Re-check +9.5V output: output must be +9.5V ± 0.3V.

**2.2.7 SUB POWER SUPPLY TEST**

Test for:

	TEST SPECIFICATION	
	+9.5V	+26V
1) Load regulation	+9.5V ± 0.3V - 0.7V	+26V+4V-1.5V
2) Voltage regulation	+9.5V ± 0.3V - 0.7V	+26V+4V-1.5V
3) Ripple voltage (at 200V AC IN)	300mV	400mV
4) Short circuit +9.5V and +26V	No Failure	No Failure
5) Standby (all rails should be at 0V)	0V	0V
6) Remote on/off operation	Hi/Low	Hi/Low

TEST LOAD	+ 9.5V	+ 26V
Max	200mA	2A
Min	50mA	0.1A

**2.2.8 C28300 POWER SUPPLY SETTING (MAIN BOARD)**

- 1) Connect DVM to +B line.
- 2) Adjust ac. mains input to 190V a.c.
- 3) Adjust VR923 and VR950 to mid-point positions.
- 4) Turn TV on and adjust VR950 until +B is as per table below.
- 5) Switch TV into standby.
- 6) Connect load A across C955 and load B across C967.
- 7) If set trips out with loads A and B then go to step 8, if not, then adjust VR923 clockwise until TV shuts down.
- 8) Disconnect a.c. mains input and loads A and B.
- 9) Connect load C across C955 and load D across C967.
- 10) Connect a.c. mains input.
- 11) If P.S.U. remains operative when switched on with loads C and D connected then alignment is correct.
- 12) If P.S.U. shuts down then alignment is incorrect. (Adjustment complete).
- 13) Disconnect a.c. mains input and loads C and D. Adjust VR923 to mid. point position and proceed from step 5.

MODEL      +B  
C28300      149.8 → 150.2V

LOAD A: +B LOAD = 180R 123W (149V)  
LOAD B: AUDIO LOAD = 10R 62W  
LOAD C: +B LOAD = 200R 111W (149V)  
LOAD D: AUDIO LOAD = 11.4 54.8W

**3.1 PICTURE POSITION/SHAPE****3.1.1 HORIZONTAL PHASE, VERTICAL CENTRE, VERTICAL AMPLITUDE**

- 1) Wait 5 minutes minimum after switching on the mains before adjustment.
- 2) Receive the Philips circle pattern.
- 3) Set brightness and contrast to maximum.
- 4) The set should face North or South.
- 5) AC input should be 230V ± SV 50Hz.
- 6) Adjust software control in service mode using

appropriate controls.

- 7) Adjust control so that the centre of the picture is as in the diagram below.

Picture Condition	Picture too low	Standard	Picture too high
Size	Adjust until upper part of castellations disappear.	Adjust until both sides of castellations disappear.	Adjust until lower part of castellations disappear.

**3.1.2 TILT, PARABOLA, WIDTH.**

- 1) Allow 5 minutes warm up time before adjustment.
- 2) Receive Philips circle pattern.
- 3) Set brightness and contrast to nominal.

4) The set should face North or South.

5) AC input should be 230V ± 5V 50Hz.

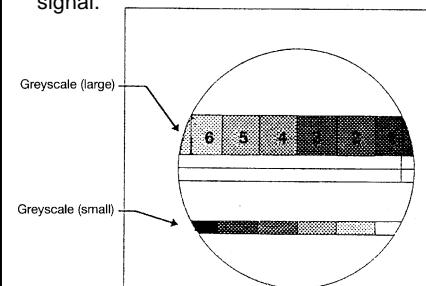
- 6) Adjust software in service mode using handset so that the vertical lines at the outside edges of the screen are adjusted to be roughly vertical.
- 7) Adjust the software in service mode using handset so that the (approximately) vertical lines at the sides of the screen are adjusted as vertical as the centre of the screen.

- 8) Adjust the software in service mode using handset so that the castellations at the sides of the picture are not quite visible. Reduce the brightness and contrast to make sure that the picture width has not reduced so that you can see beyond the castellations. You may have to repeat stages 6 and 7 again.

**3.2 FOCUS ADJUSTMENT**

- 1) Receive the Philips circle pattern.
- 2) Adjust after horizontal/vertical has been adjusted.

3) Switch the received signal to the cross hatch signal.



- 4) Turn the focus VR gradually clockwise from the full counter clockwise position so that the focus of the vertical line in the centre part, furthest to the right is adjusted for best result (contrast - maximum, brightness - normal).</li

## Voltage Tables

IC001	
PIN	VOLTAGE
1	4.3V (Power good)
2	0V (AV1 switch)
3	4.7V (R/C input)
4	0V (AV2 switch)
5	Use scope shift clock
6	Use scope shift clock
7	N/C
8	0V Mute
9	Use scope shift enable
10	5V D.E.N.
11	0V Customer mode (5V Service mode)
12	4.7V Out of STBY (0V in stand by)
13	1.7V OSC.OUT
14	0V VSS
15	0V Vss
16	Use scope OSC Out
17	N/C
18	5V Reset
19	5V A8
20	5V A9
21	5VA10
22	0V N/C
23	5V A11
24	0V A12
25	0V A13
26	5V A14
27	0V A15
28	Use scope D0/A0
29	Use scope D1/A1
30	Use scope D2/A2
31	Use scope D3/A3
32	Use scope D4/A4
33	Use scope D5/A5
34	Use scope D6/A6
35	Use scope D7/A7
36	GND
37	GND
38	GND
39	5V P/D
40	4.7V SDA
41	4.7V SCL
42	5V VDD
43	5V VDD
44	Use scope A5
45	Use scope D5
46	Use scope RW
47	Use scope SDI
48	OV LED DIM (Norm 5V LED Bright stand by)
49	N/C
50	N/C
51	4.6V Mode switch position A
52	V VOL -
53	5V VOL +
54	5V PROG -
55	SV PROG +
56	5V SVHS switch (OV when SVHS plug inserted)

Pins marked as "use scope" are digital pulses between OV and 5V giving erratic readings using a multi-meter or digital multi-meter.

IC002	
PIN	VOLTAGE
1	5V (Vcc)
2	4.7V (P/D)
3	1.4V (A9)
4	3.3V (A8)
5	1.9V (A4)
6	2.1V (A8)
7	1.7V(A6)
8	1.6V (A7)
9	1.8V (A0)
10	1.7V (A1)
11	1.8V (A2)
12	1.7V (A3)
13	2.5V (D3)
14	2.6V (D2)
15	2.4V (D1)
16	OV (GND)
17	2.7V (D0)
18	1.9V (D7)
19	1.7V (D6)
20	2.5V (D5)
21	1.7V (D4)
22	0V (CE)
23	0V (A15)
24	2.2V (OE)
25	0V (A14)
26	4.4V (A13)
27	4.7V (A12)
28	3.0V (A11)
29	1.4V (A10)
30	5.0V (Vcc)
31	5.0V (Vcc)
32	5.0V (Vcc)

Voltages taken using a D.M.M. Data (D) and address (A) lines being digital pulse between OV and +5V.

IC003	
PIN	VOLTAGE
1	OV (OE)
2	1.8V (A0)
3	2.6V (D0)
4	2.4V (D1)
5	1.8V (A1)
6	1.9V (A2)
7	2.5V (D2)
8	2.5V (D3)
9	1.8V (A3)
10	0V (GND)
11	1.0V (LE)
12	1.6V (A7)
13	1.8V (D6)
14	1.8V (D7)
15	1.7V (A6)
16	2.1V (AS)
17	2.5V (DS)
18	1.7V (D4)
19	2.0V (A4)
20	5.0VC (Vcc)

Voltages taken using a D.M.M. Data (D) and address (A) lines being digital pulse between OV and +5V.

IC004	
PIN	VOLTAGE
1	1.8V
2	0.1V
3	2.7V
4	2.3V
5	1.9V
6	0.1V
7	0V
8	5.0V
9	5.0V
10	4.3V (Power good)
11	0V
12	0V(SE)
13	0V(DS)
14	0V(SD)
15	SV TV/ 0V AV1 / 5V AV2 / 0V AV3 (ASEL1)
16	SV(Vcc)

Voltages taken using a D.M.M. Data (D) and address (A) lines being digital pulse between OV and +5V.

IC401	
PIN	VOLTAGE
1	3.8V(RFL)
2	3.0V(AVL)
3	3.8V (Audio out L.)
4	2.9V(AV3.L.)
5	3.1V(AV2.L.)
6	0V(GND)
7	0V(GND)
8	0V(GND)
9	0V TV/ 8V (AV1) 0V (AV2) 8V (AV3)
10	0VT/ DV (AV1) 8V (AV2) 8V (AV3)
11	2.9V(AV3.R.)
12	3.9V(RF.R.)
13	3.9V (Audio out R.)
14	3V(AV2.R.)
15	3V(AV1.R.)
16	8V(Vdd)

IC101	
PIN	VOLTAGE
Input	21.8V
GND	0V
Output	12V

IC601	
PIN	VOLTAGE
1	2.3V (V. Drive)
2	2.2V (V. Drive)
3	8.9V
4	18V (+18V Supply)
5	8.8V
6	N/C
7	0V(GND)
8	49.7V (V. Output supply)
9	9.3V (V. Output)
10	2.4V(V.Pulse)
11	6.3V (E.W. Output)
12	0.7V (EW. Input)
13	0V(GND)

IC902	
PIN	VOLTAGE
1	12.6V
2	li.4V
3	N/C
4	-8.2V
S	-8.iV
6	N/C

IC901	
PIN	VOLTAGE
1	137.2V
2	137.0V
3	N/C
4	- 8.2V
S	0.6V
6	N/C

IC9101	
PIN	VOLTAGE
1	7.3V
2	6.2V
3	N/C
4	
5	
6	
7	
8	
9	
10	
11	
12	
13	
14	
15	
16	

IC950	
PIN	VOLTAGE
Input	12.SV
GND	0V
Output	5V

IC951	
PIN	VOLTAGE

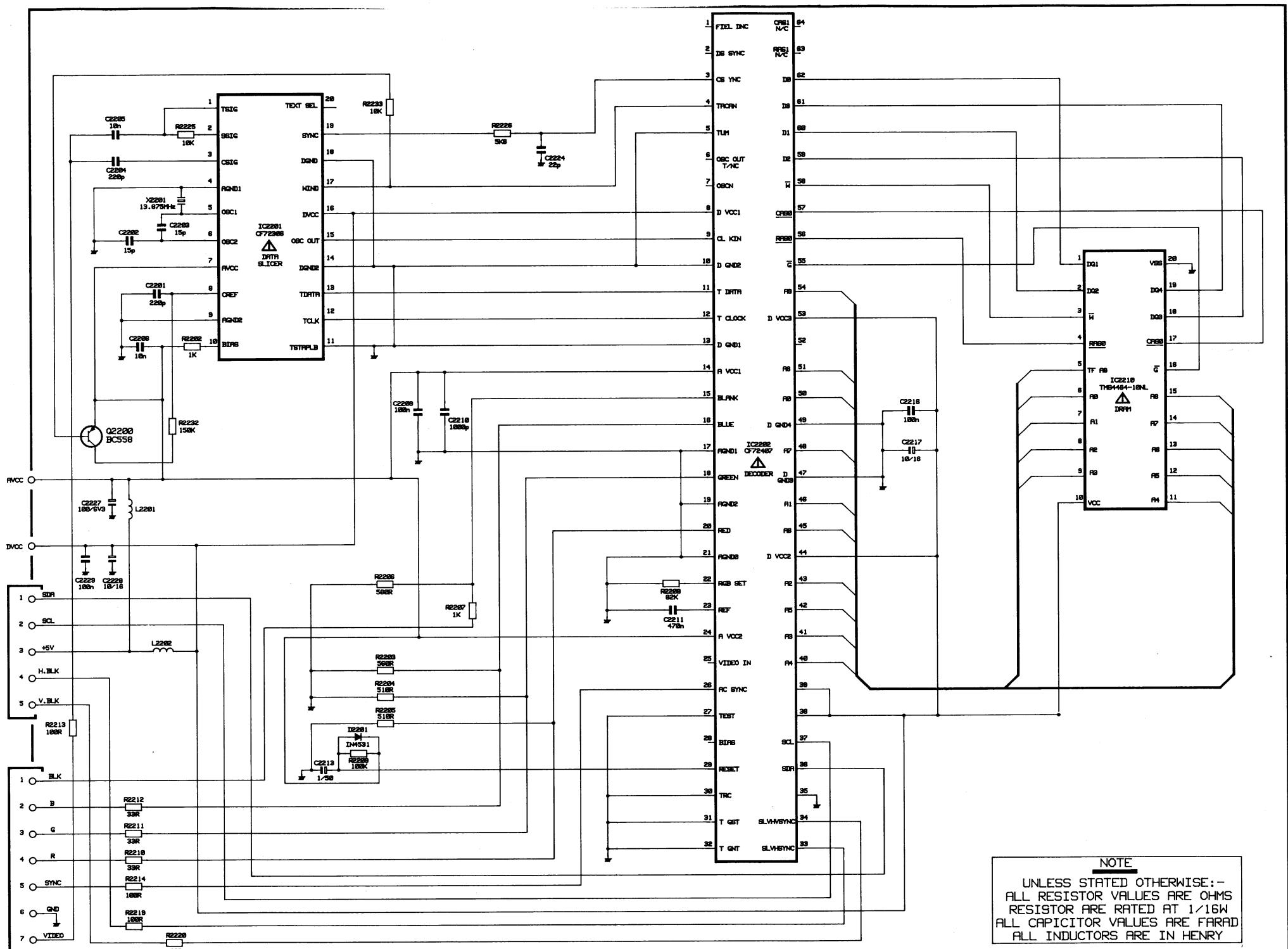



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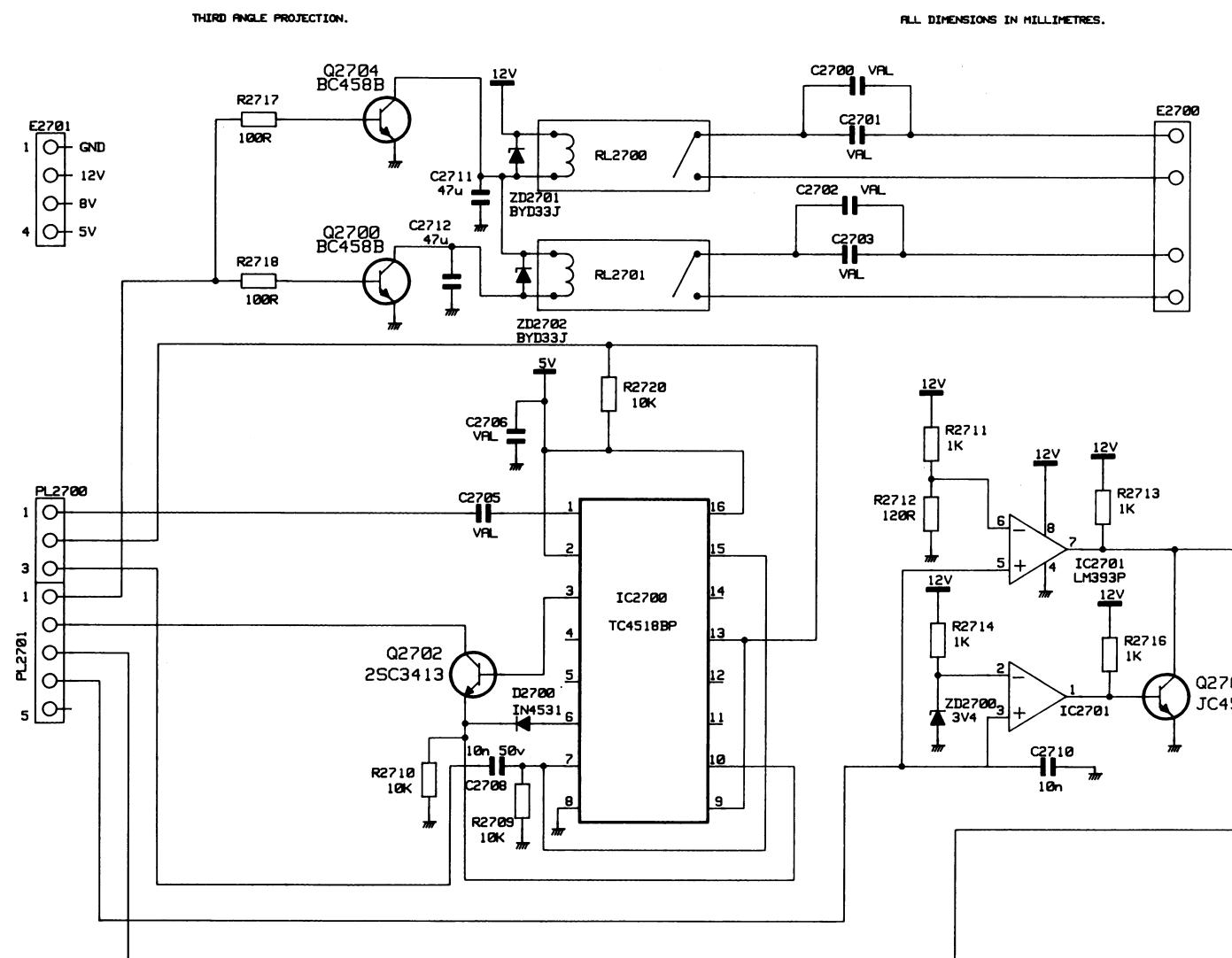
## Voltage Tables Cont'd

In	<b>Q001</b>	<b>Q0D2</b>	<b>Q003</b>	<b>Q101</b>					
Control	5V	DV	5V	Dependent on agc					
Out	0V	2.2V	-1.9V	0V					
	5V	DV	5V	Dependent on agc					
B	<b>Q201</b>	<b>Q202</b>	<b>Q203</b>						
C	3.8V	DV	2.3V						
E	12.1V	DV	1.6V						
	3V	DV	4.9V						
B	<b>Q301</b>	<b>Q302</b>	<b>Q3D3</b>	<b>Q304</b>	<b>Q305</b>	<b>Q306</b>	<b>Q307</b>		
C	3.6V	3.9V	4.9V	3.7V	0V	2.6V	7.3V		
E	7.3V	7.4V	4.9V	7.9V	0V	7.6V	S.6V		
	2.9V	3.2V	0V	3V	0.7V	1.9V	7.9V		
B	<b>Q402</b>	<b>Q403</b>	<b>Q405</b>	<b>Q4D6</b>	<b>Q407</b>	<b>Q4D8</b>	<b>Q409</b>		
C	4.9V	4.9V	4.4V	4.6V	3.7V	3.7V	3.6V		
E	0V	0V	12V	12V	3.1V	3.iV	2.9V		
	0V	0V	3.8V	3.9V	7.9V	7.9V	7.9V		
B	<b>Q410</b>	<b>Q411</b>	<b>Q412</b>						
C	3.5V	3.7V	3.7V						
E	2.9V	3.1V	3V						
	7.9V	7.9V	7.9V						
B	<b>Q501</b>	<b>Q502</b>	<b>Q510</b>						
C	3.3V	7.3V	12.3V						
E	7.3V	5.1V	DV						
	2.7V	7.9V	12.2V						
B	<b>Q701</b>	<b>Q705</b>	<b>Q751</b>						
C	-1V	3.iV	0.5V						
E	33.3V	8.2V	164.5V						
	0V	2.8V	0.6V						
B	<b>Q810</b>	<b>Q811</b>	<b>Q812</b>	<b>Q813</b>					
C	11.7V	8V	7.9V	7.6V					
E	12.5V	7.3V	7.2V	6.8V					
	11.0V	9.9V	0.1V	9.9V					
B	<b>Q851</b>	<b>Q852</b>	<b>Q856</b>	<b>Q857</b>	<b>Q859</b>				
C	0.7V	0.1V	3.6V	3.7V	0.4V				
E	0.5V	S.8V	7.SV	7.SV	0V				
	6.5V	11V	4.3V	4.3V	1V				
B	<b>Q860</b>	<b>Q861</b>	<b>Q862</b>	<b>Q863</b>					
C	D.4V	0.4V	2.1V	3.7V					
E	DV	DV	3.2V	0V					
	1V	1V	1.4V	4.3V					
B	<b>Q901</b>	<b>Q902</b>	<b>Q903</b>	<b>Q904</b>					
C	-3.2V	0.6V	-4.2V	-8V					
E	-4.6V	-3.2V	398V	-4.6V					
	-8.1V	0V	0V	-8.1V					
B	<b>Q950</b>	<b>Q961</b>	<b>Q952</b>	<b>Q954</b>	<b>Q953</b>	<b>Q955</b>	<b>Q959</b>		
C	27.2V	27.9V	0.7V	6.8V	0.7V	3.2V	0.2V		
E	12.7V	28.7V	0V	138.1V	0V	3.2V	37.2V		
	12.6V	28.7V	0V	6.2V	0V	0V	0V		
B	<b>Q4201</b>	<b>Q4202</b>							
C	2.2V	2.3V							
E	4.4V	4.6V							
	1.6V	1.6V							
B	<b>Q4300</b>	<b>Q4301</b>							
C	0.7V	0V	Woofer sat at mid.						
E	0V	0V							
	0V	0V							
B	<b>Q4400</b>	<b>Q4401</b>							
C	4V	4V							
E	8V	8V							
	3.3V	3.3V							
B	<b>Q4501</b>								
C	0.1V								
E	15.3V								
	0V								
B	<b>Q4450</b>	<b>Q4451</b>							
C	4V	4V							
E	8.2V	8.2V							
	3.3V	3.3V							
B	<b>Q4600</b>	<b>Q4601</b>	<b>Q46D4</b>	<b>Q4605</b>					
C	4.4V	4.4V	4.4V	4.4V					
E	3.7V	3.7V	3.7V	3.7V					
	9V	9V	9V	9V					
B	<b>Q4700</b>	<b>Q4800</b>	<b>Q4801</b>						
C	0V	0V	0.7V						
E	0V	0V	0V						
	14.3V	12.5V	0V						
B	<b>Q5000</b>								
C	3.8V								
E	3.1V								

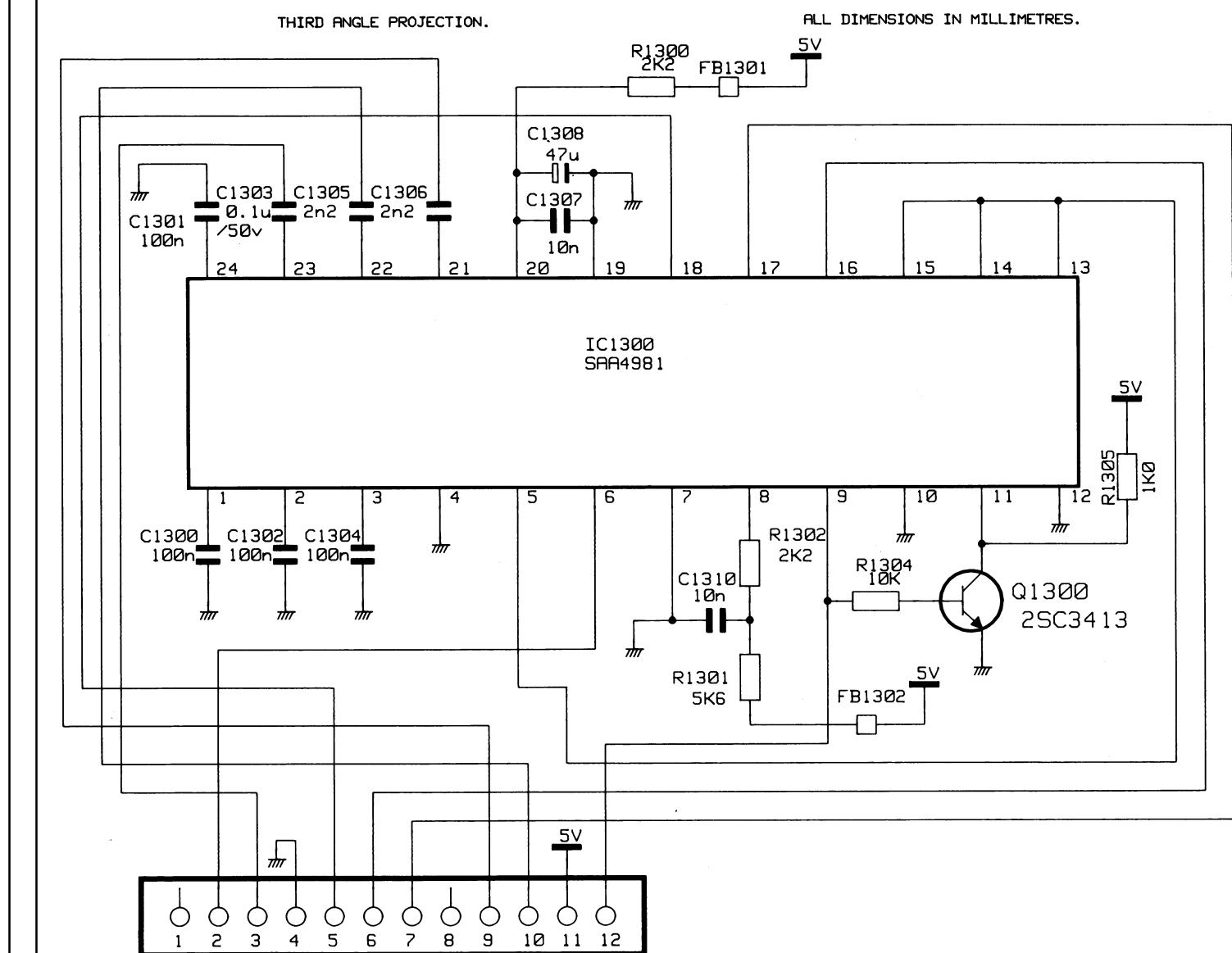
# Text Diagram



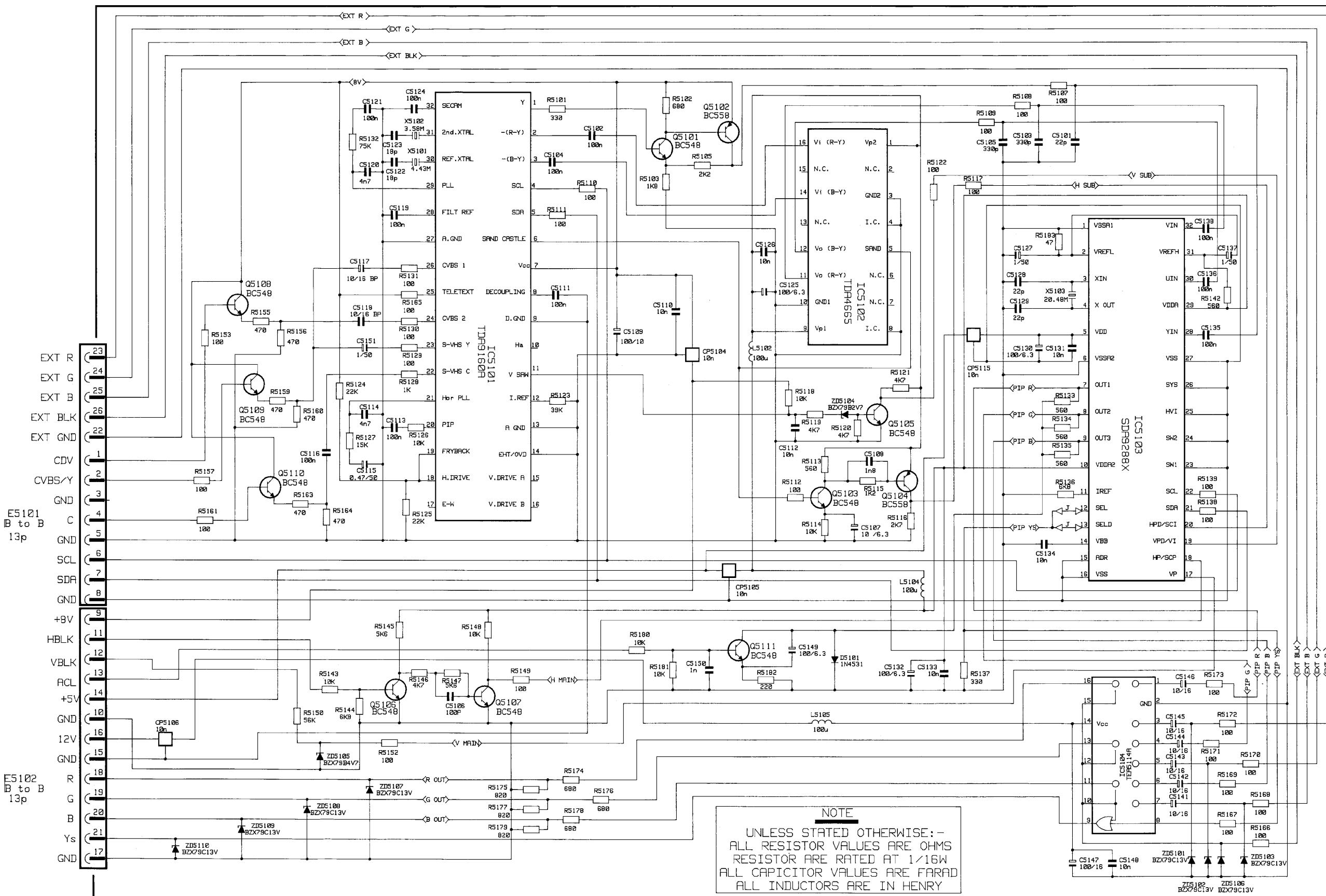
## 16:9 Deflection PCB Diagram



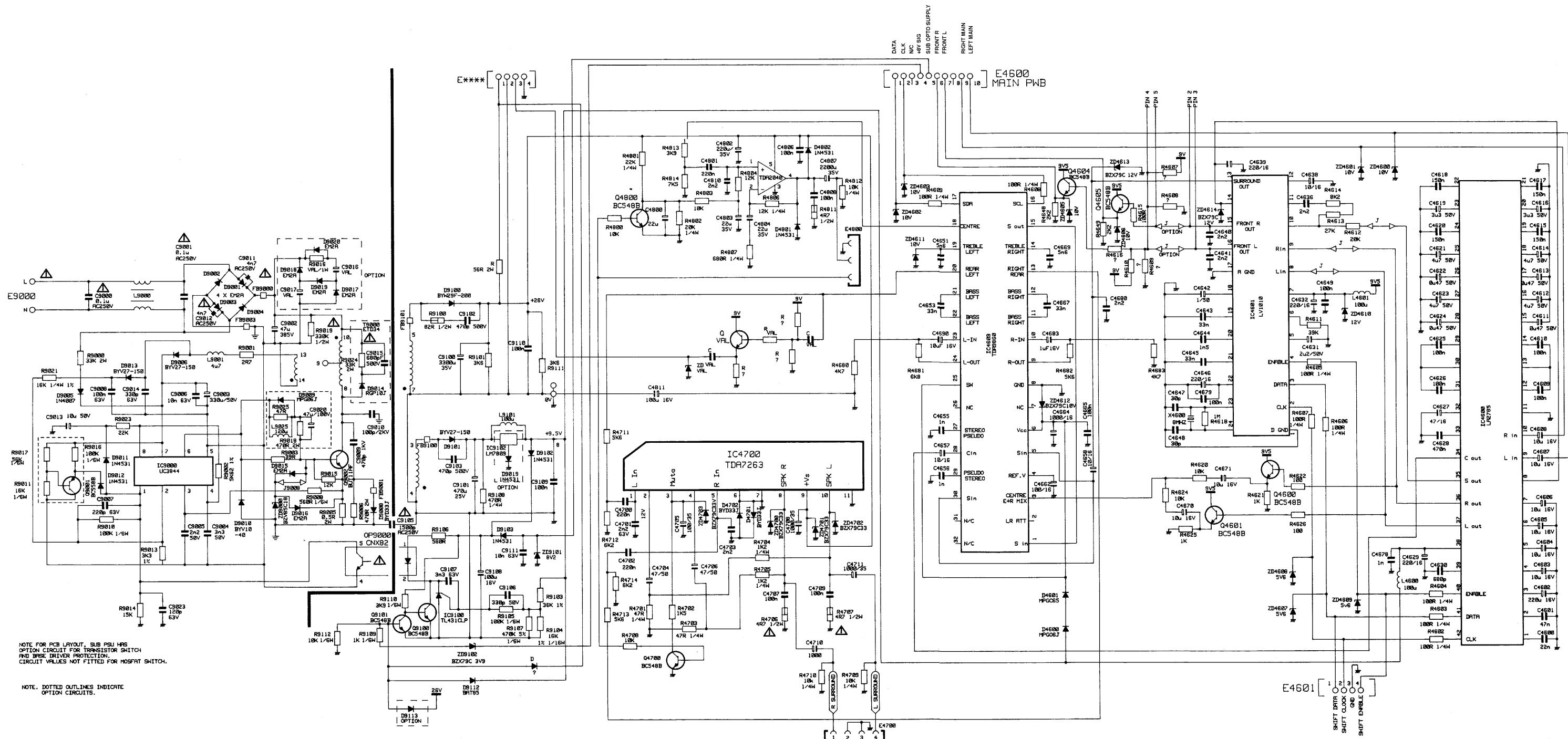
## Compression Circuit Diagram



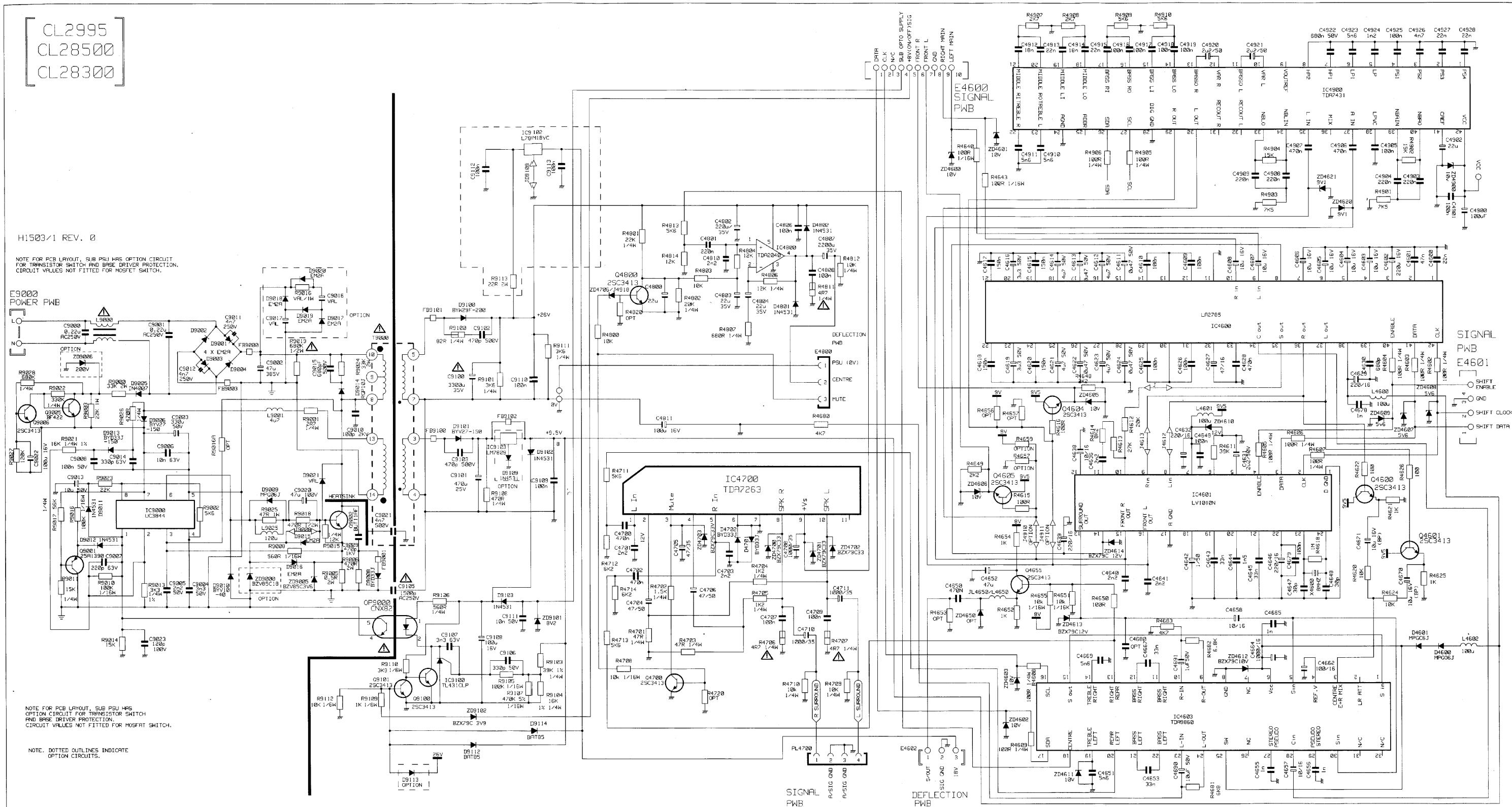
# RGB Processing Diagram



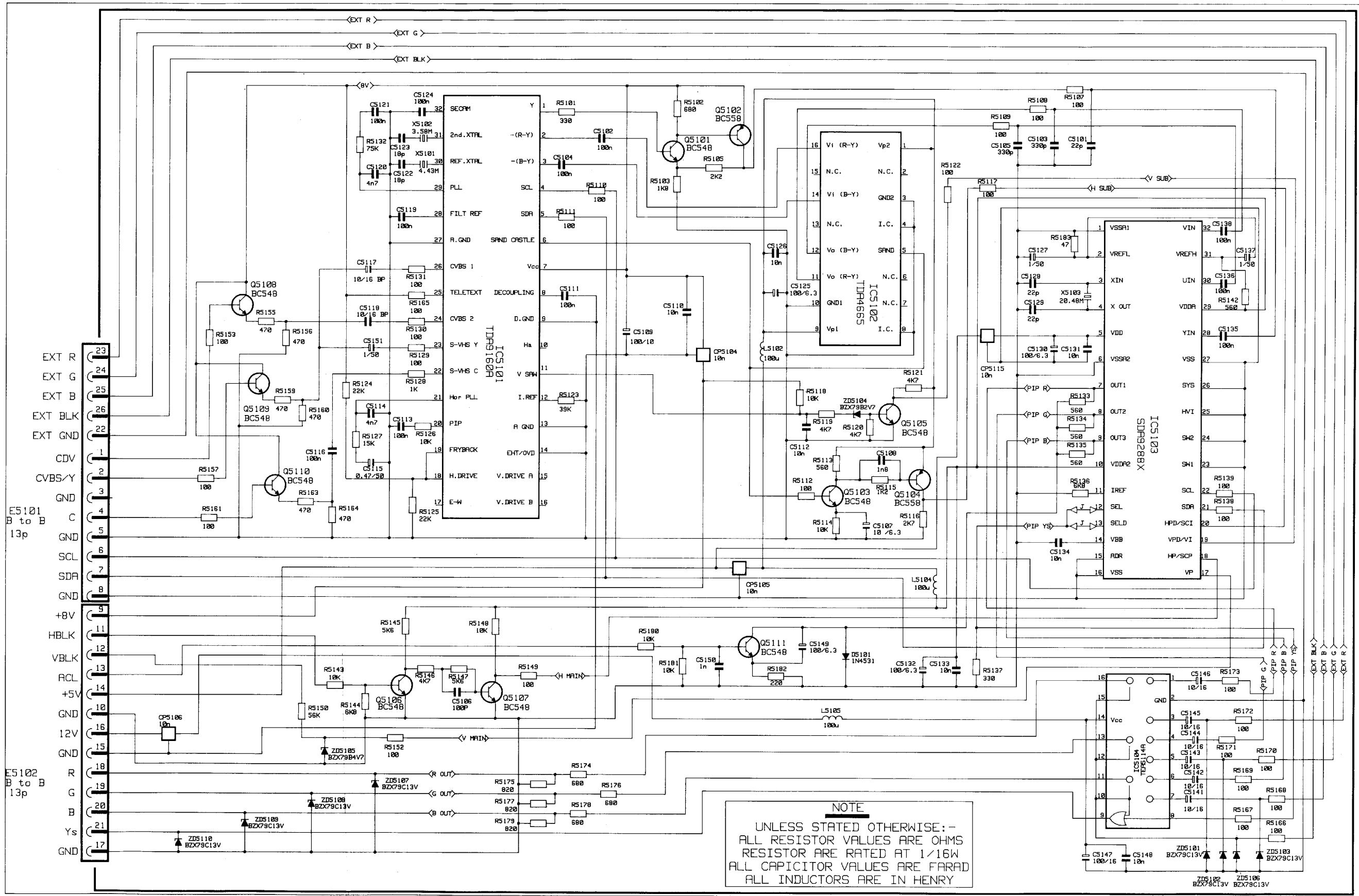
## Dolby Audio Diagram 1



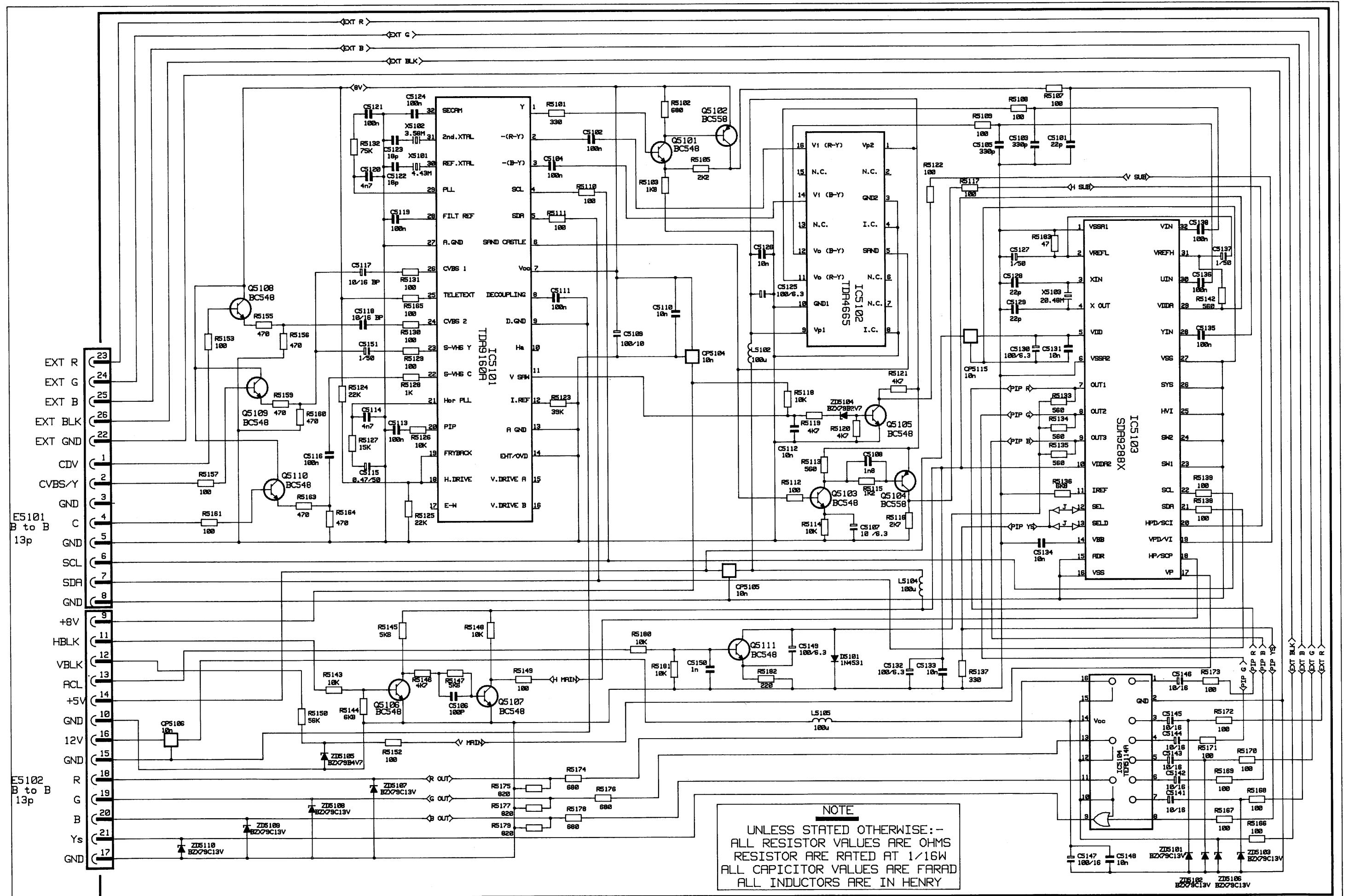
## Dolby Audio Diagram



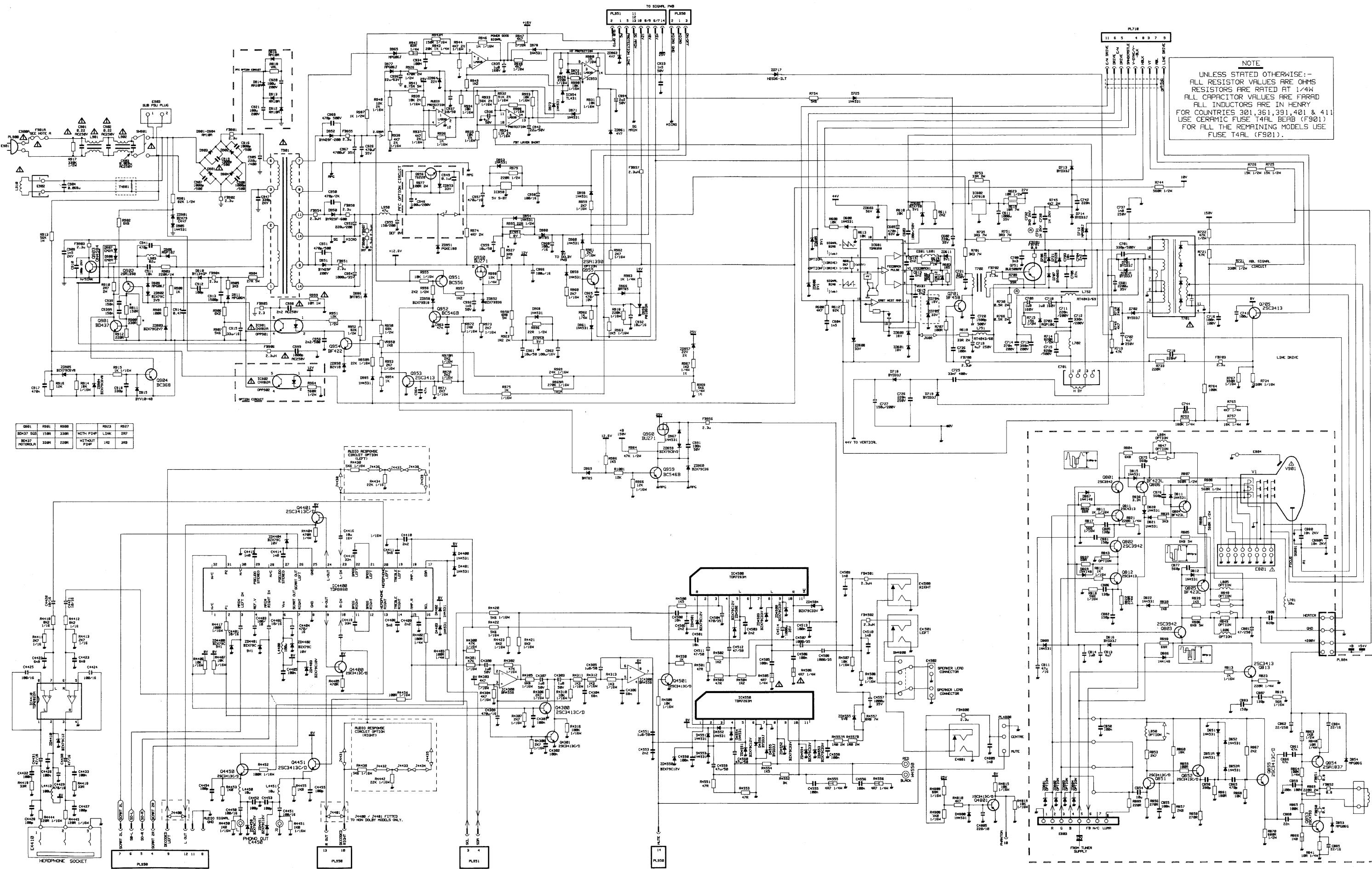
# PIP Circuit Diagram



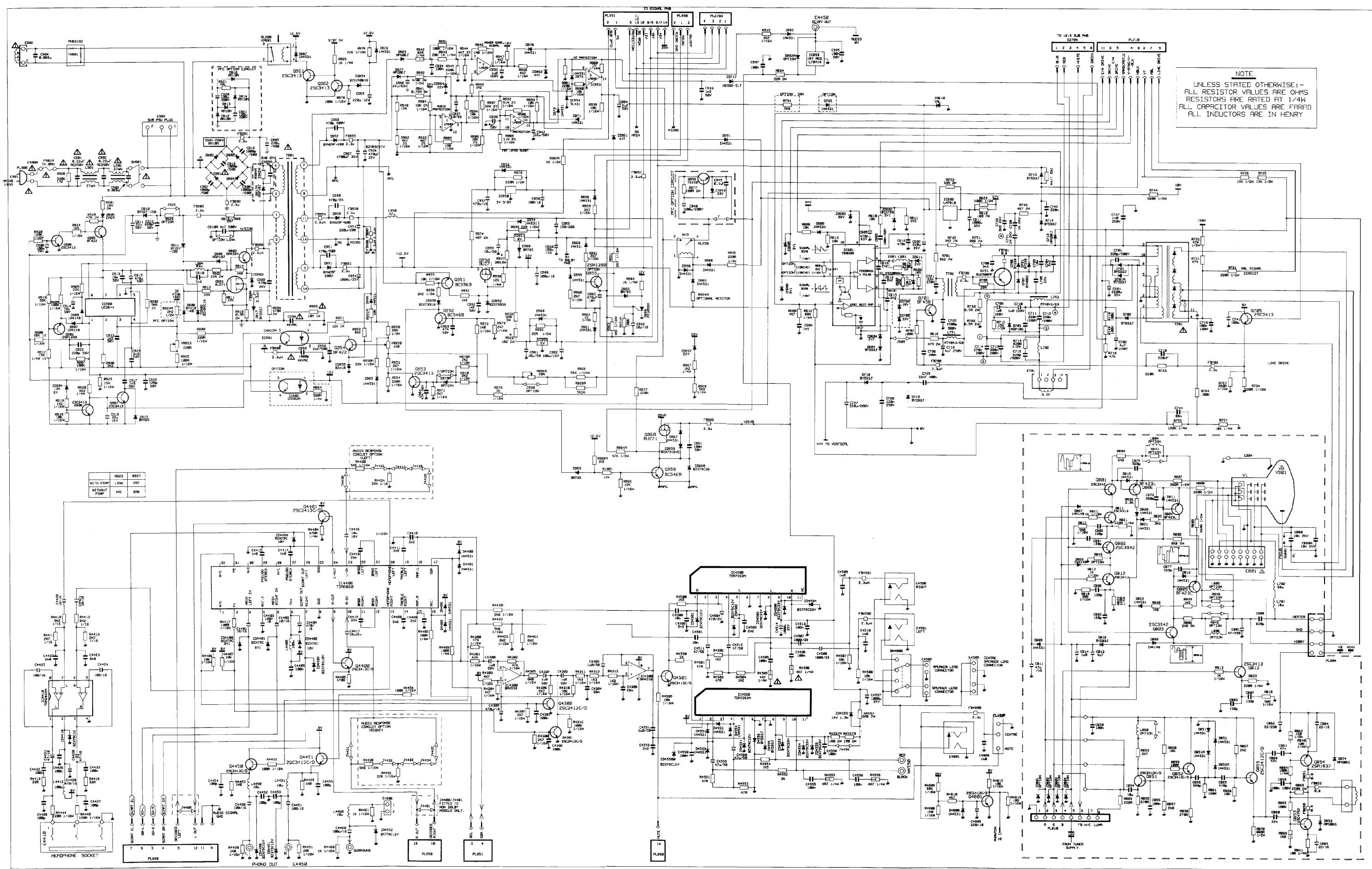
## PIP Circuit Diagram 2



## Power &amp; Deflection Diagram 1

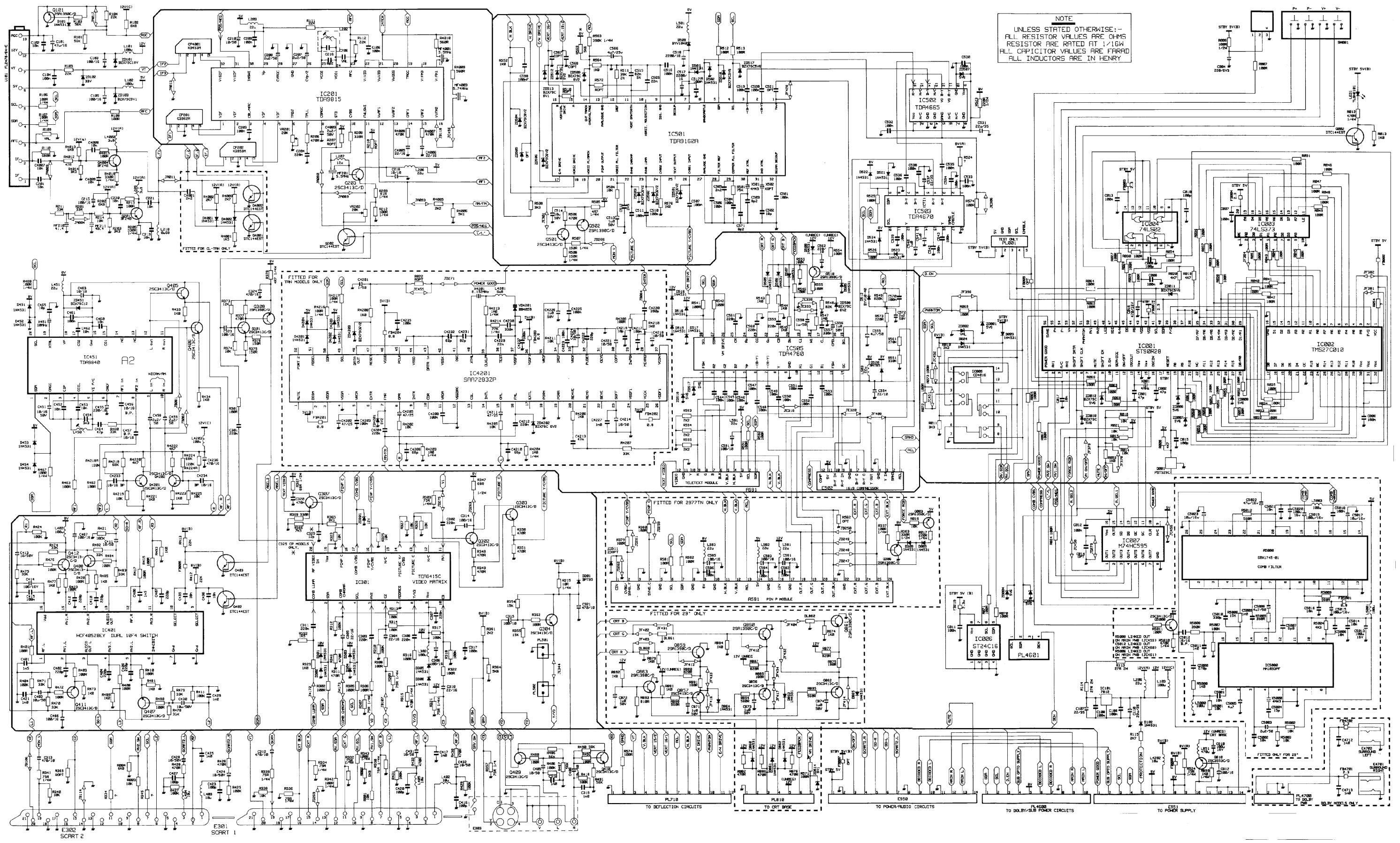


Power & Deflection Diagram 2 (C28300)

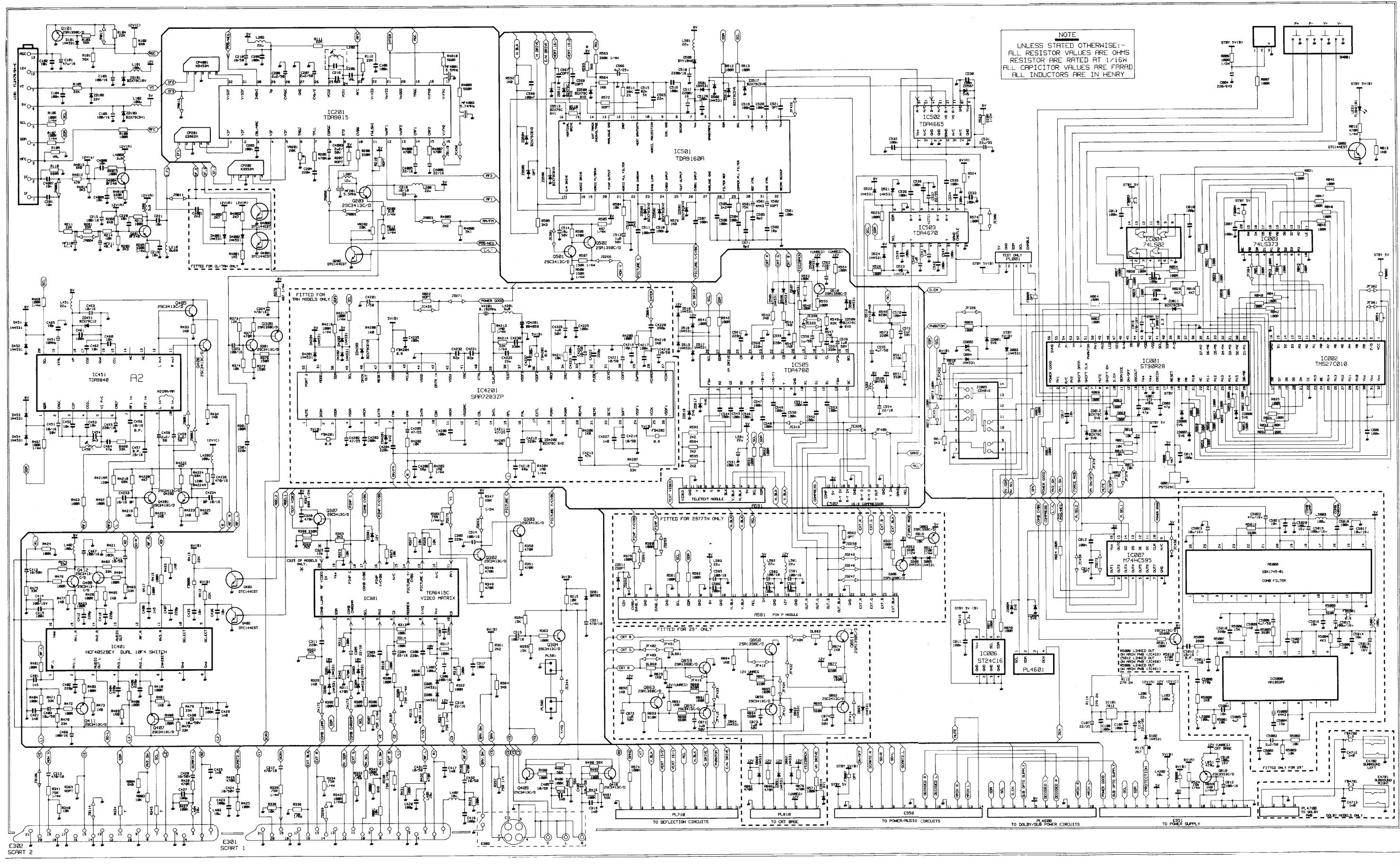


# Signal Processing Diagram

## (C2576/77TN - 311, CP2576TAN-301/351, CP2576TAN-331/381)



**Signal Processing Diagram**  
**(CL2576, CP2576, CL2976, CP2976, C2976TA, C2975TAN, C2976TN)**



# Signal Processing Diagram (C28300)

