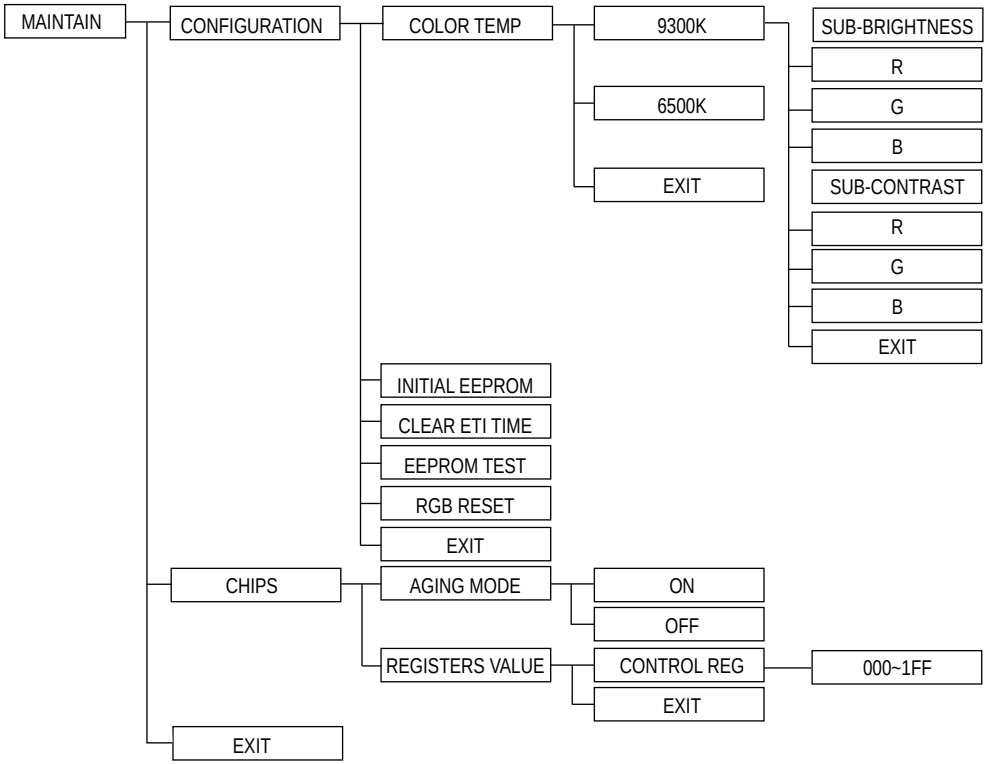
2-2. Uses of Service Mode

1. Turn off the power with the POWER button in the front panel. The red POWER LED lights on.
2. Press the POWER button in the front panel with pressing the UP (↑) and DOWN (↓) buttons, and the system enters the service mode.
   When no signal is input to the selected input terminal, the following functions are disabled.
3. Press the MENU button, and the main menu is displayed. Then go to page 2 of the main menu with the UP (↑) or DOWN (↓) button, then select the MAINTAIN icon on the bottom line, and then press the OK button.
4. In this menu screen, the version number and released date of the internal software can be checked.
5. The structure of the MAINTAIN menu is shown below.



The operation procedure is basically same as that of the ordinary user controls.
The function of each menu is explained in next section.

2-3. Functions of Service Mode

1. COLOR TEMP
   This is used for the white balance adjustment at color temperature 9300 K and 6500 K. The adjustment requires to be done for every input (INPUT1: DVI-D, INPUT1: HD15, or INPUT2: HD15) by switching the input. The adjustment data is stored into the register for respective inputs.

2. INITIAL EEPROM
   This sets the data of the EEPROM to the default data. This operation is not required usually.

3. CLEAR ETI TIME
   This resets the ETI (Elapsed Time Indicator) counter to 00000 H.

4. EEPROM TEST
   This tests writing and reading of the EEPROM.

5. RGB RESET
   This adjust the offset and gain of the input AD converter for the analog inputs (INPUT1: HD15 and INPUT2:HD15). As these adjustments are common to both inputs, perform them for either input. It is unnecessary for another input.
   Execute the adjustments under the condition where the signal specified in "White Balance Adjustment" is input.

6. AGING IN MODE
   This sets and clears the NO SYNC AGING flag.
   AGING MODE = ON: Sets the NO SYNC AGING flag.
   AGING MODE = OFF: Clears the NO SYNC AGING flag.
   When the NO SYNC AGING flag is set and the input with no input signal is selected, the system goes into the AGING MODE. The NO SYNC AGING flag is held until it is cleared. To clear the NO SYNC AGING flag, go into the service mode and then set the AGINGN MODE to OFF, or execute the all mode recall.

7. CONTROL REG.
   This can check the data of the internal registers. This operation is not required usually.

2-4. White Balance Adjustment

1. Preparation
   (1) Measurement point : Center of screen
   (2) Measurement distance : 50 cm
   (3) Measurement angle : 90°
   (4) Color analyzer (Minolta CS-1000 or equivalent)
   (5) Signal generator (Astro Design VG-828D or equivalent)
   Be sure to calibrate the analog RGB output level with 75Ω termination.

2. Service mode setting
   Enter the service mode referring to step 1 and 2 of Section 2-2.

3. Aging
   Set the AGING MODE in the service mode to ON. Disconnect the signal input terminal or select the input with no signal input, and the system goes into the AGING MODE.
   Execute aging for 30 minutes or more.

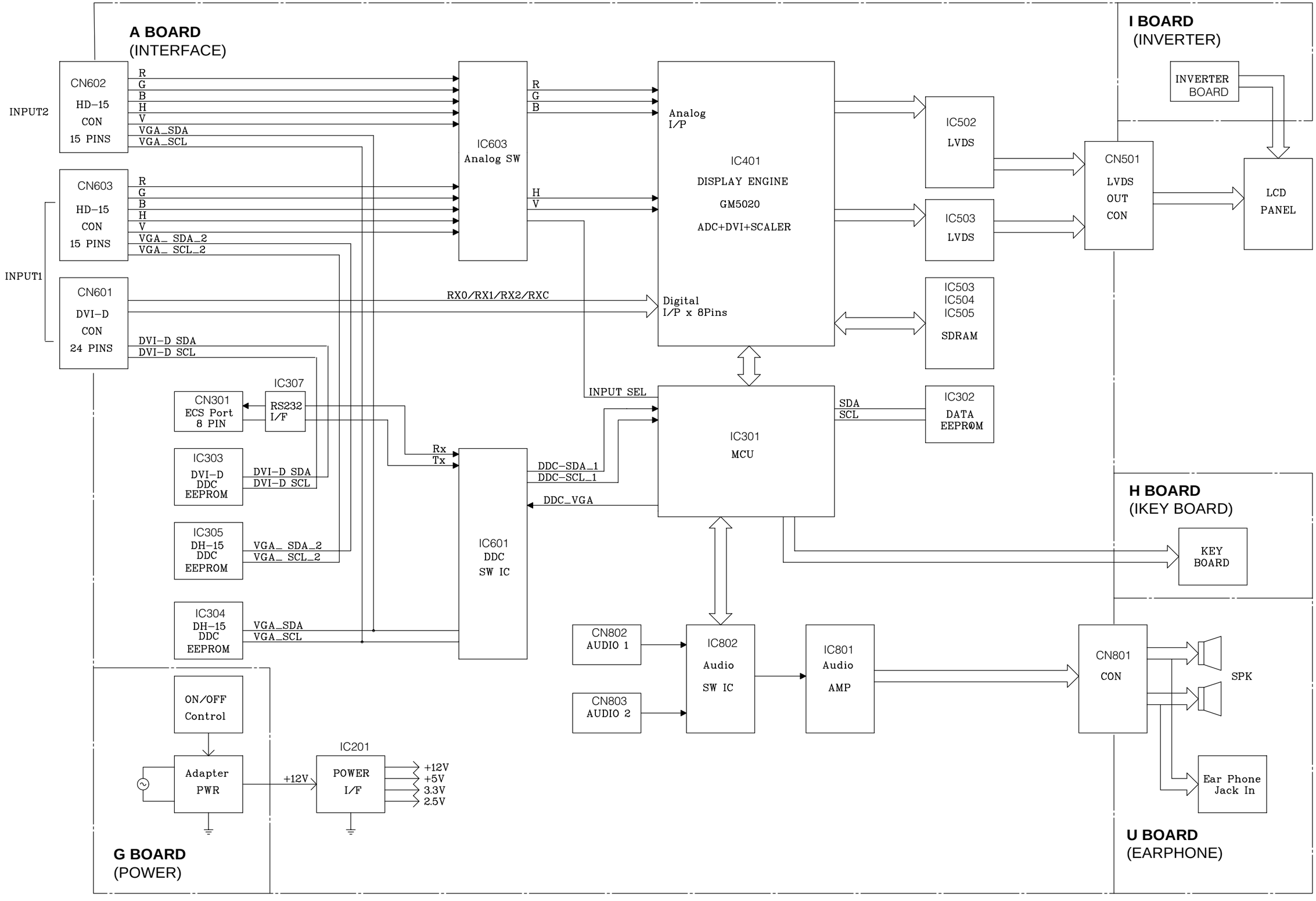
4. User control setting
   Feed a signal to the selected input, and then execute reset in the menu screen. Then, move the menu display position to avoid the measurement point. Or, set the following for respective inputs.

   BACKLIGHT (Brightness of backlight) = 100
   CONTRAST = 70
   BRIGHTNESS = 50
   Menu display position = not center of screen (Avoid the measurement point.)
   (The setting of the menu display position is common to respective inputs.)
   ECO = OFF (The setting of ECO is common to respective inputs.)

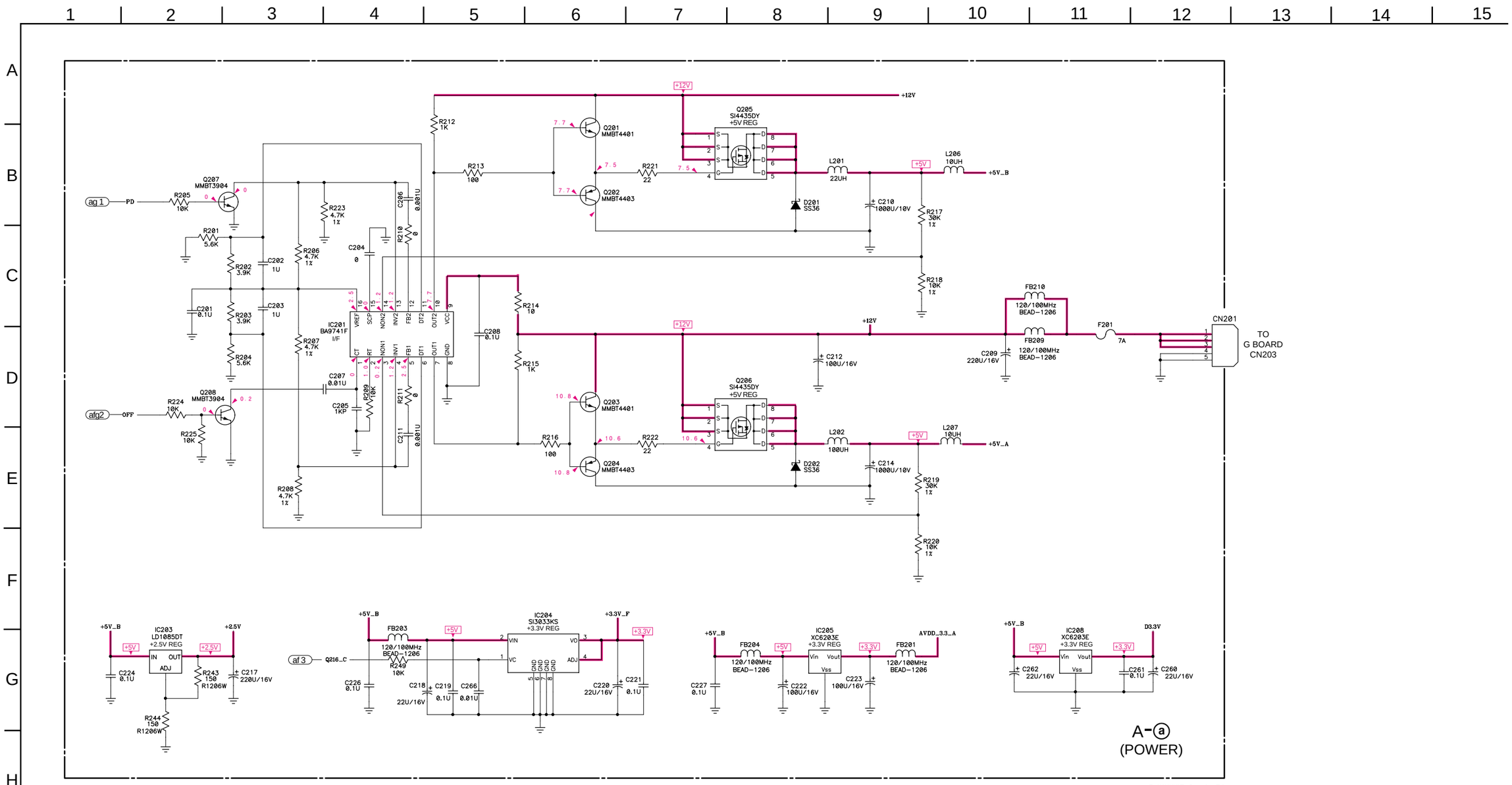
   Do not change the above setting until the white balance adjustment is completed.

SECTION 3
DIAGRAMS

3-1. BLOCK DIAGRAMS



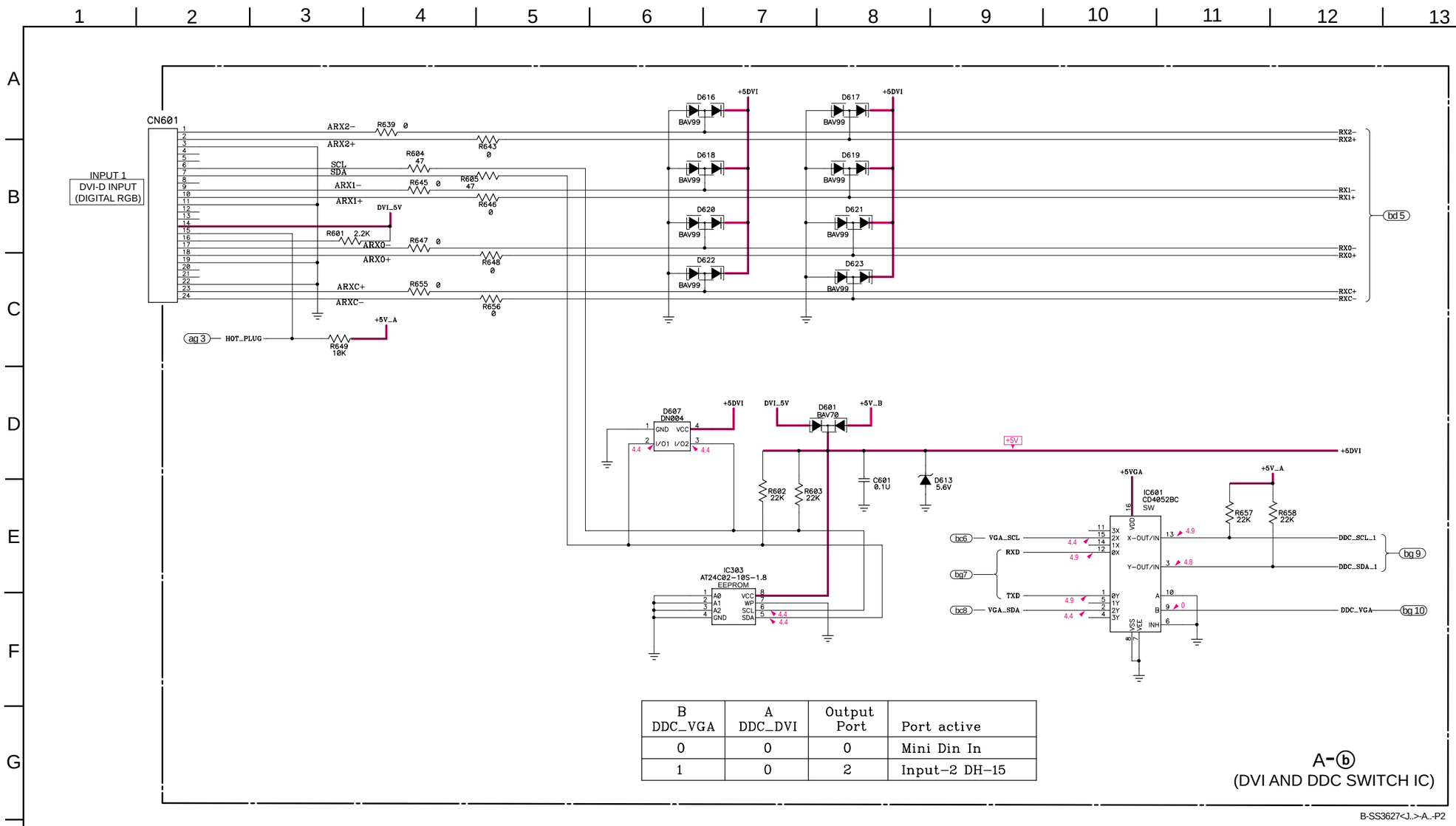
(1) Schematic Diagrams of A (ⓐ, ⓑ, ⓒ, ⓓ, ⓔ, ⓕ, ⓖ) Board



B-SS3627<J..>-A..-P1

- Divided circuit diagram
  One sheet of A board are circuit diagram is divided into eight sheets, each having the code A-ⓐ to A-ⓖ. For example, the destination ab1 on the code A-ⓐ sheet is connected to ab1 on the A-ⓑ sheet.

  a b 1
  - 1 → Ref. No.
  - a → Circuit diagram division code

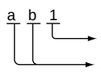


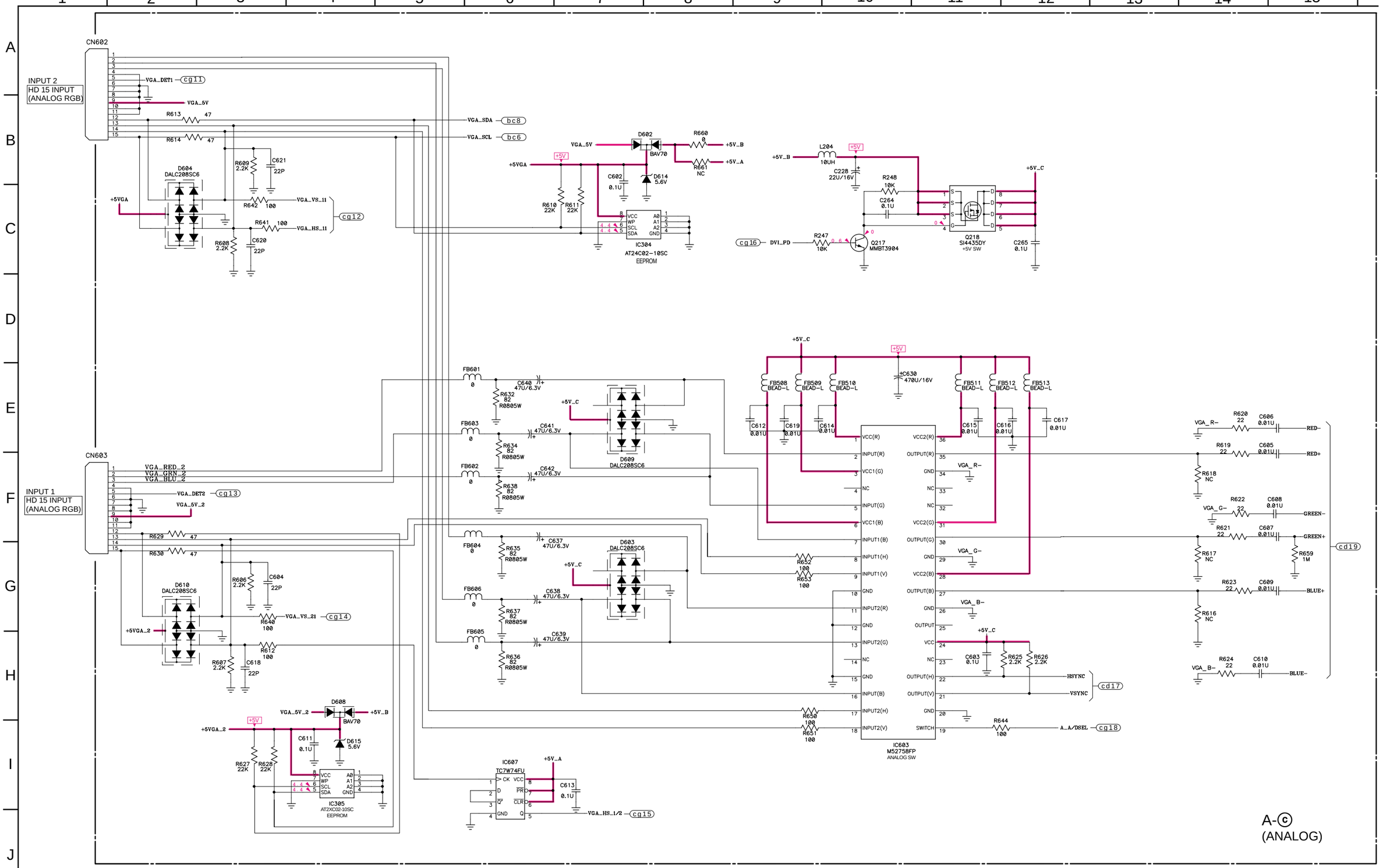
B DDC_VGA	A DDC_DVI	Output Port	Port active
0	0	0	Mini Din In
1	0	2	Input−2 DH−15

A-ⓑ
(DVI AND DDC SWITCH IC)

B-SS3627<J..>-A..-P2

- Divided circuit diagram
  One sheet of A board are circuit diagram is divided into eight sheets, each having the code A-ⓐ to A-ⓘ. For example, the destination (ab1) on the code A-ⓐ sheet is connected to (ab1) on the A-ⓑ sheet.



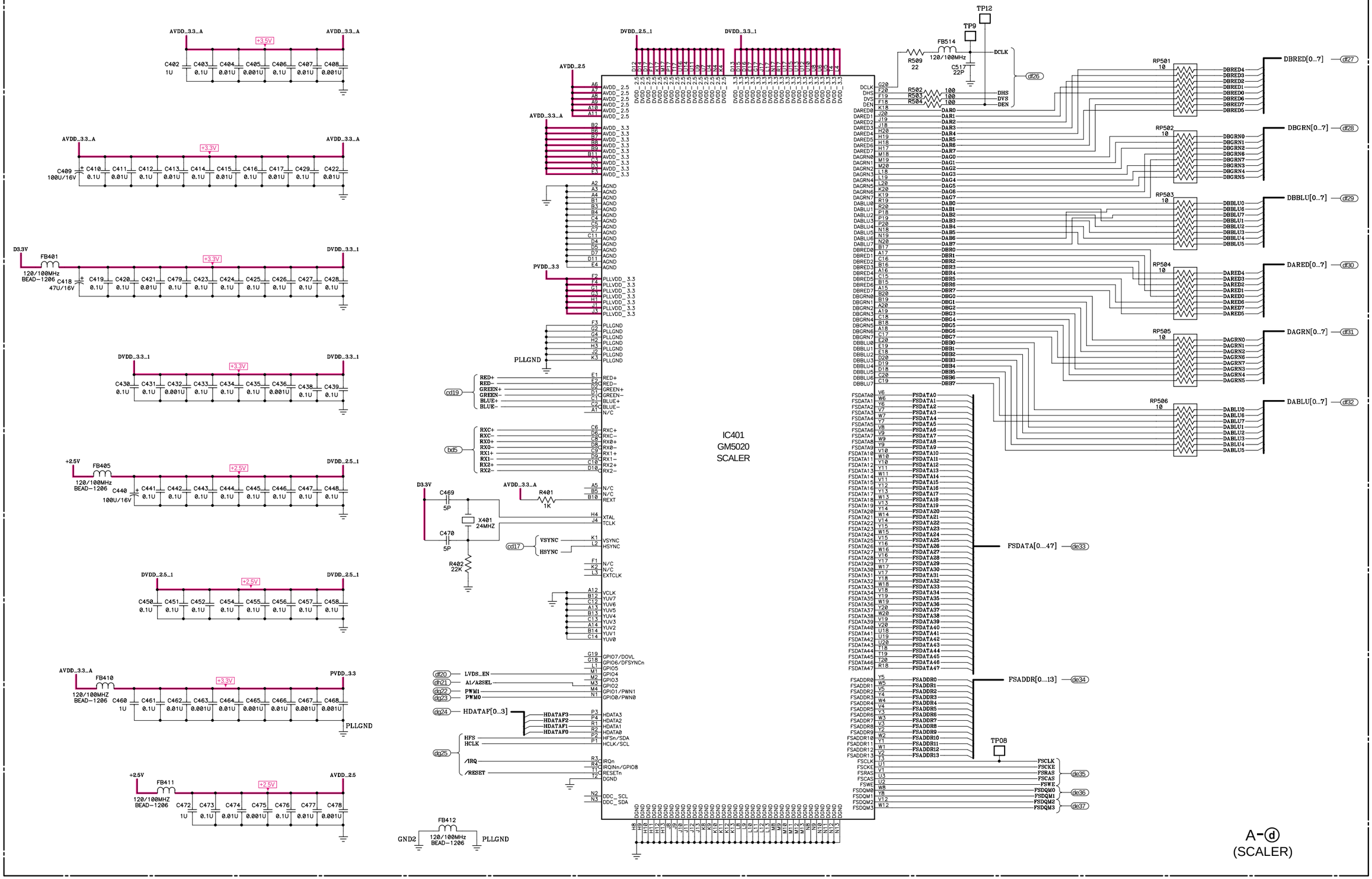


B-SS3627<J..>-A..-P3

- Divided circuit diagram

  One sheet of A board are circuit diagram is divided into eight sheets, each having the code A-ⓐ to A-ⓖ. For example, the destination ab1 on the code A-ⓐ sheet is connected to ab1 on the A-ⓑ sheet.

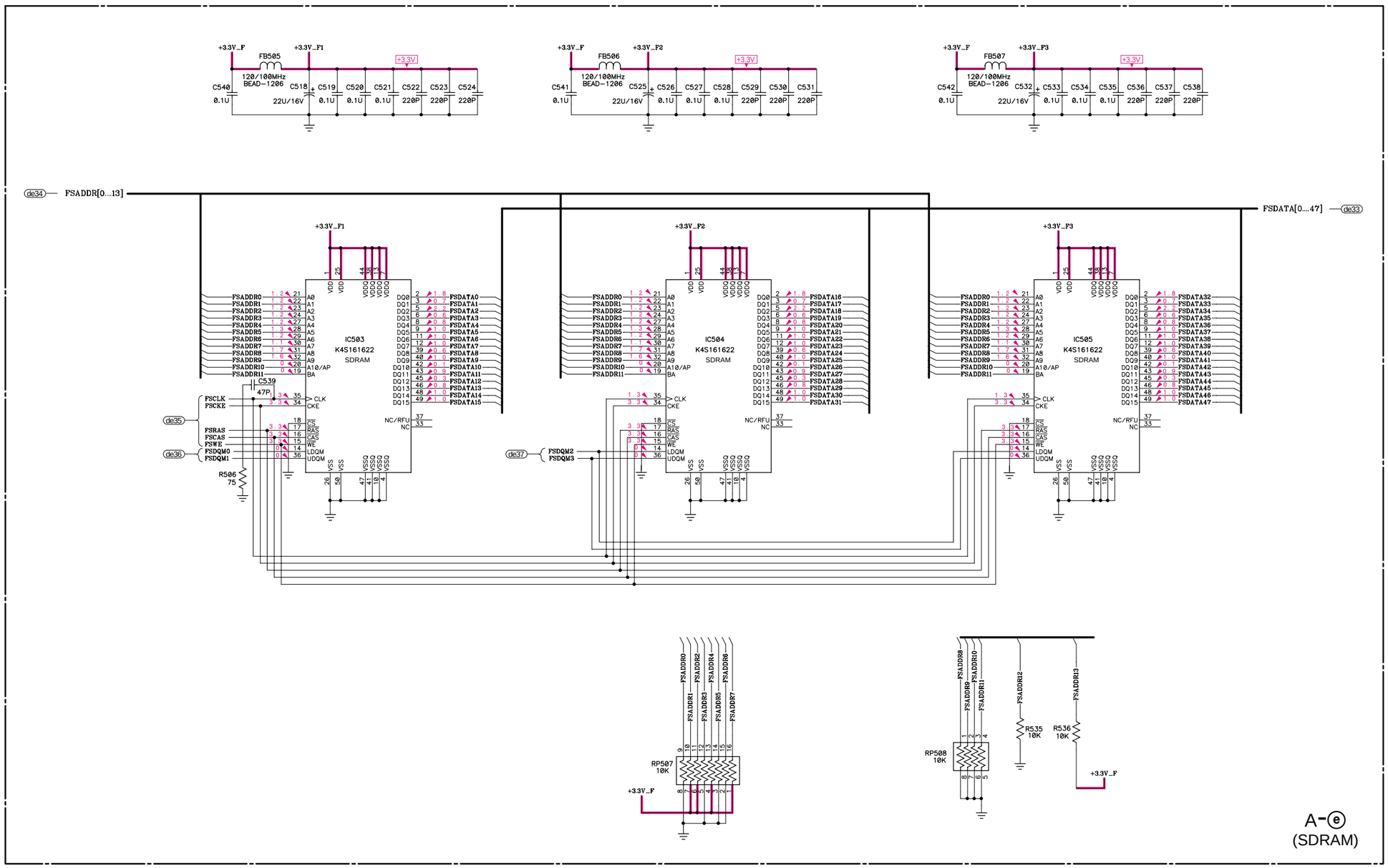
  a b 1
  - Ref. No.
  - Circuit diagram division code



- Divided circuit diagram
  One sheet of A board are circuit diagram is divided into eight sheets, each having the code A-ⓐ to A-ⓖ. For example, the destination (ab1) on the code A-ⓐ sheet is connected to (ab1) on the A-ⓑ sheet.

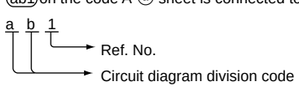
a b 1
- Ref. No.
- Circuit diagram division code

B-SS3627<J..>-A..-P4

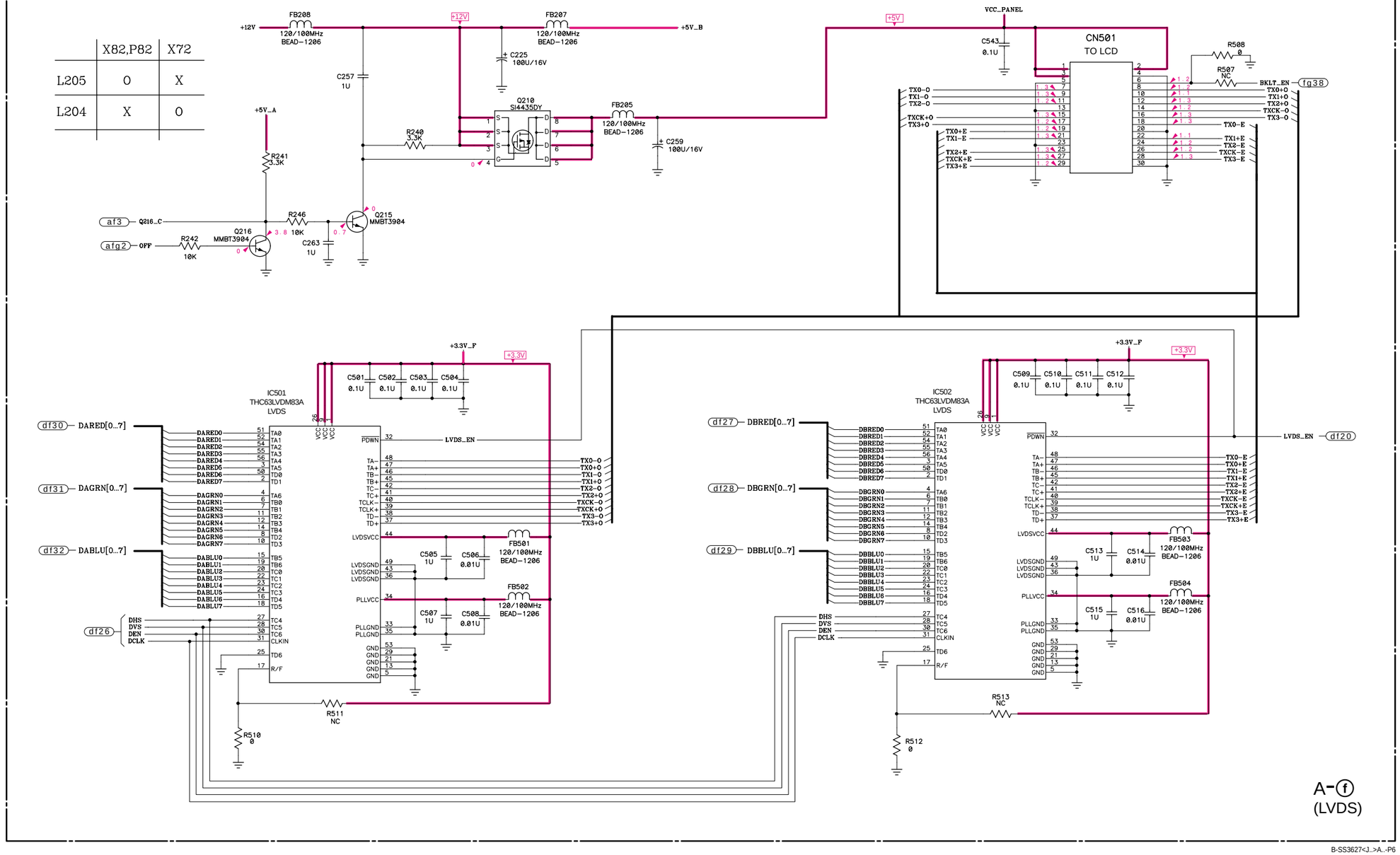


B-SS3627<J..>-A..-P5

- Divided circuit diagram
  One sheet of A board are circuit diagram is divided into eight sheets, each having the code A-ⓐ to A-ⓘ. For example, the destination ab1 on the code A-ⓐ sheet is connected to ab1 on the A-ⓑ sheet.

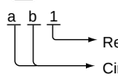


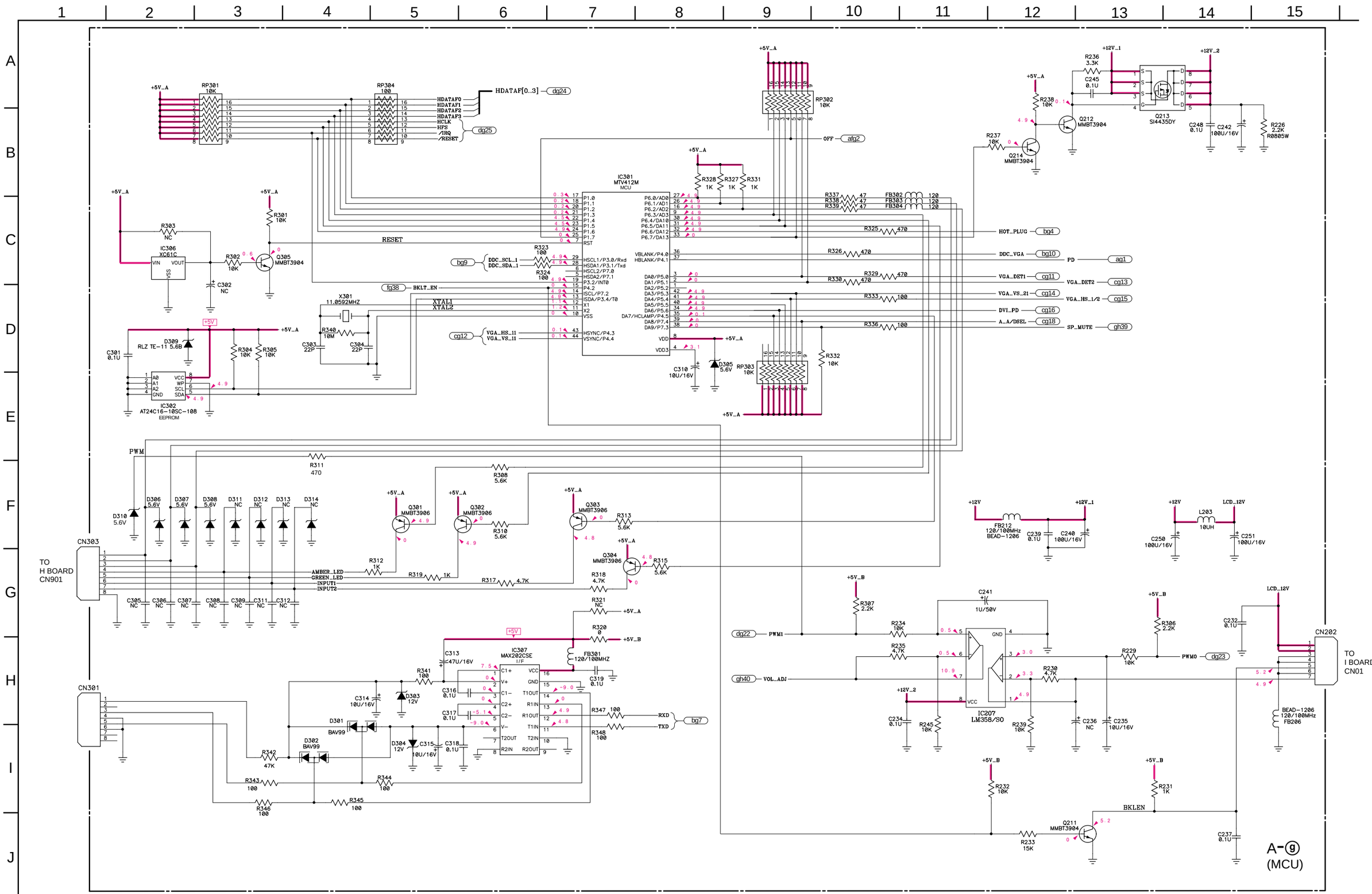
	X82,P82	X72
L205	0	X
L204	X	0



B-SS3627<J..>A..-P6

- Divided circuit diagram
  One sheet of A board are circuit diagram is divided into eight sheets, each having the code A-ⓐ to A-ⓖ. For example, the destination ab1 on the code A-ⓐ sheet is connected to ab1 on the A-ⓑ sheet.





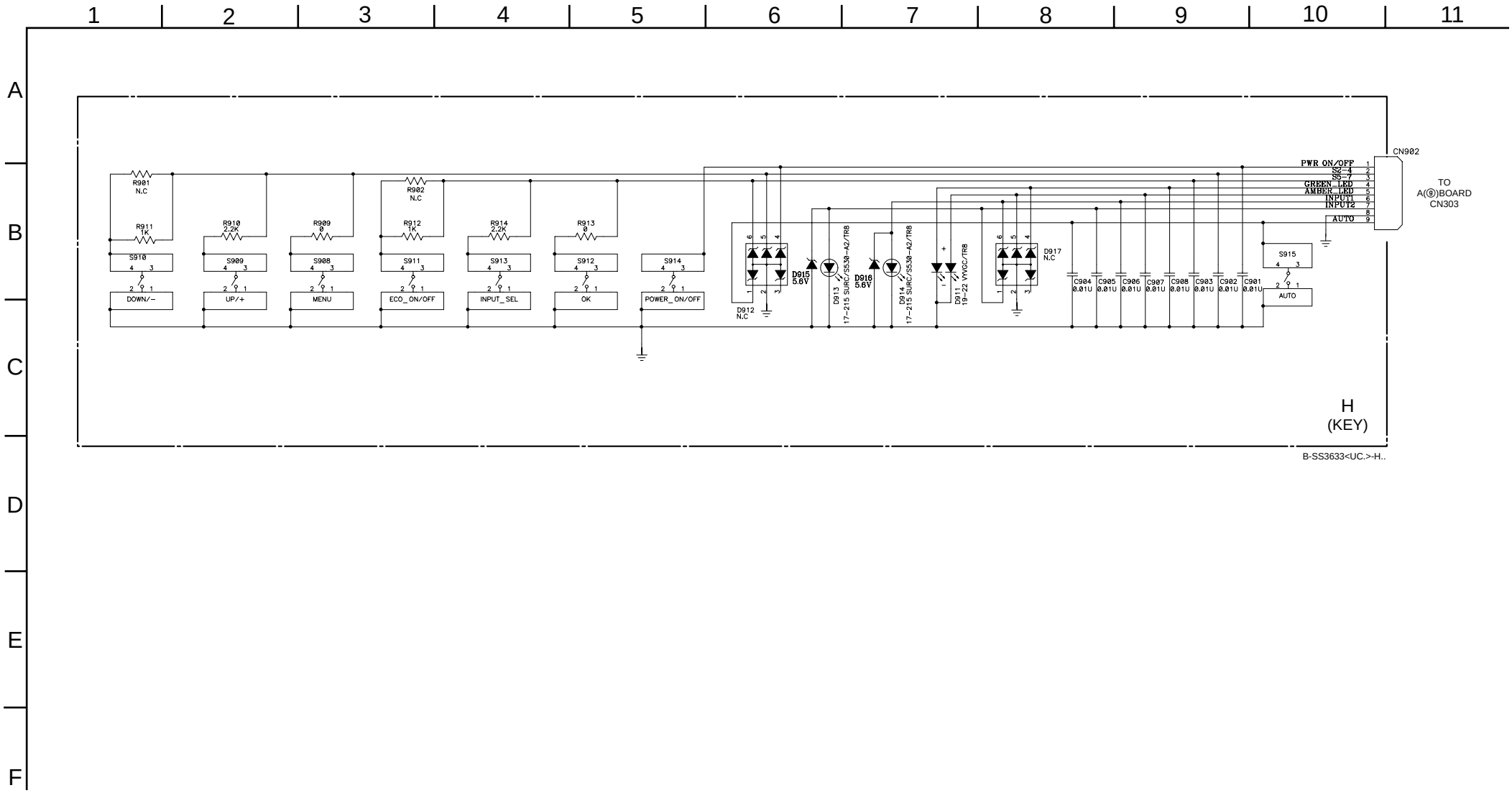
B-SS3627<J..> - A..-P7

- Divided circuit diagram

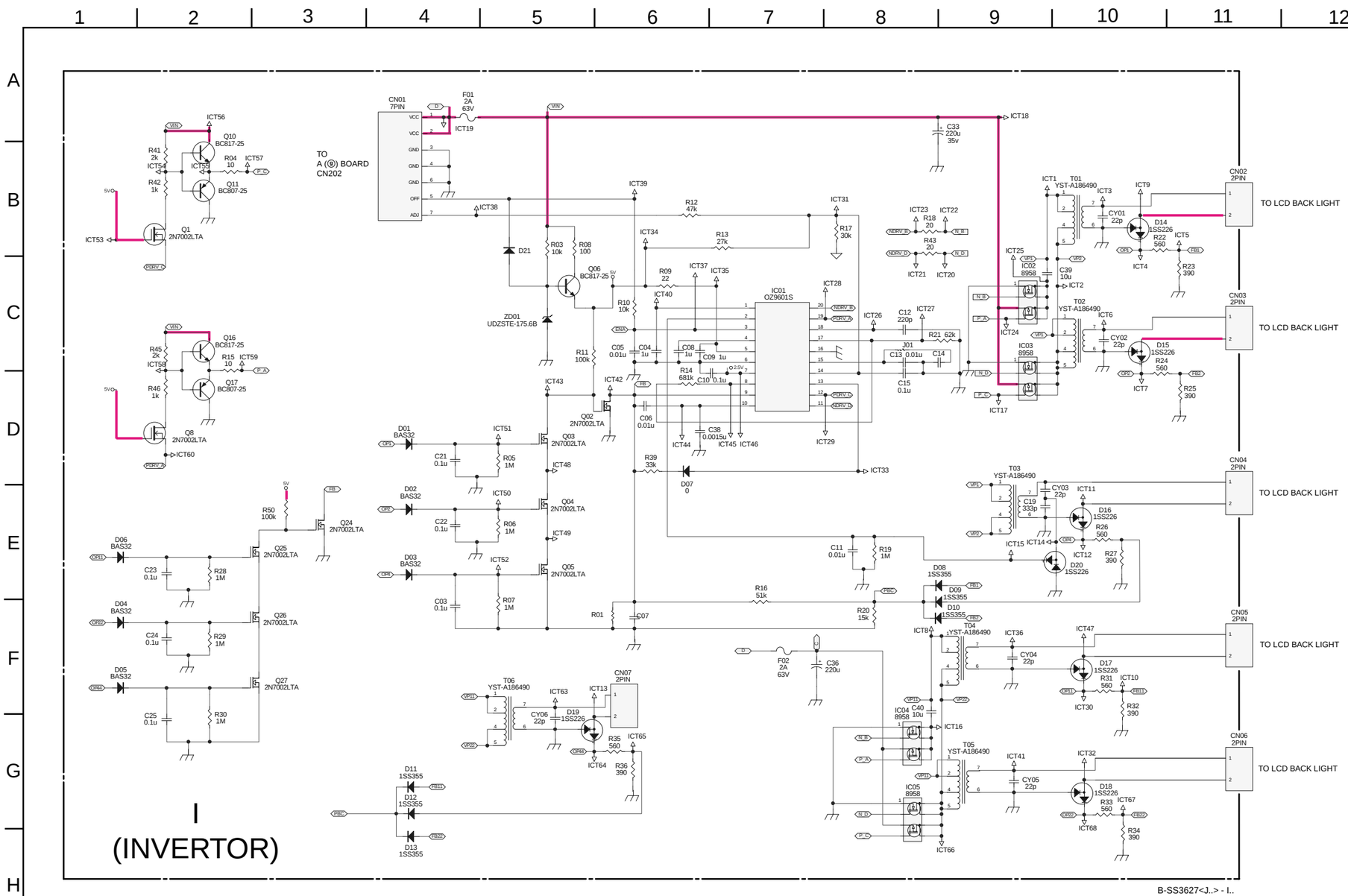
  One sheet of A board are circuit diagram is divided into eight sheets, each having the code A-ⓐ to A-ⓖ. For example, the destination (ab1) on the code A-ⓐ sheet is connected to (ab1) on the A-ⓑ sheet.

  a b 1 → Ref. No.
  Circuit diagram division code

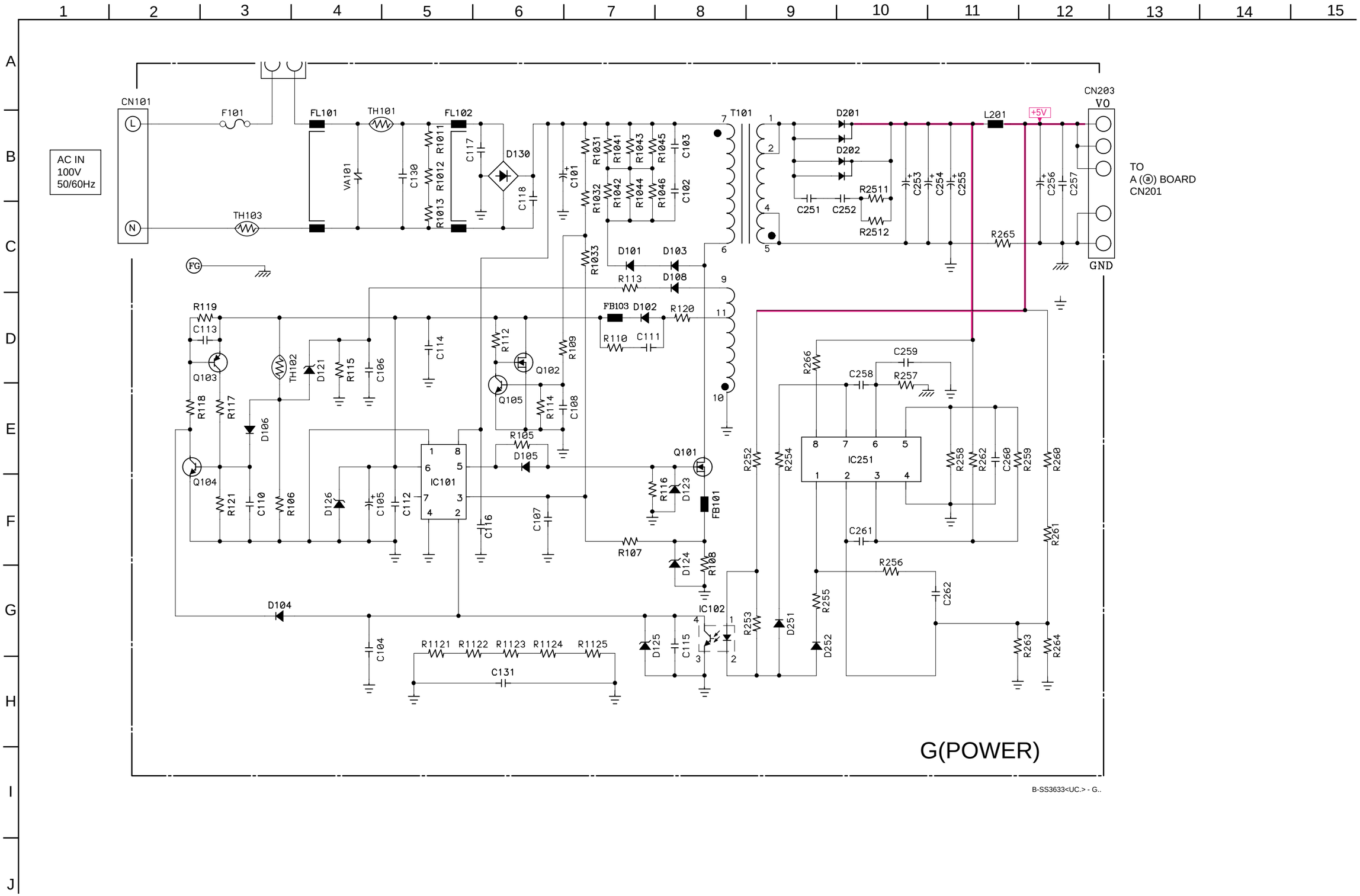
(2) Schematic Diagram of H Board



(3) Schematic Diagrams of I Board



(4) Schematic Diagrams of G Board



B-SS3633<UC.> - G..