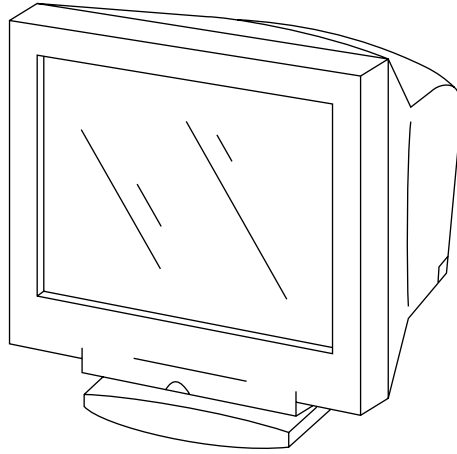


GDM-5410

SERVICE MANUAL



Japan Model
N.Hemisphere Model
S.Hemisphere Model
Chassis No. SCC-L22L-A

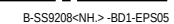
G1 CHASSIS

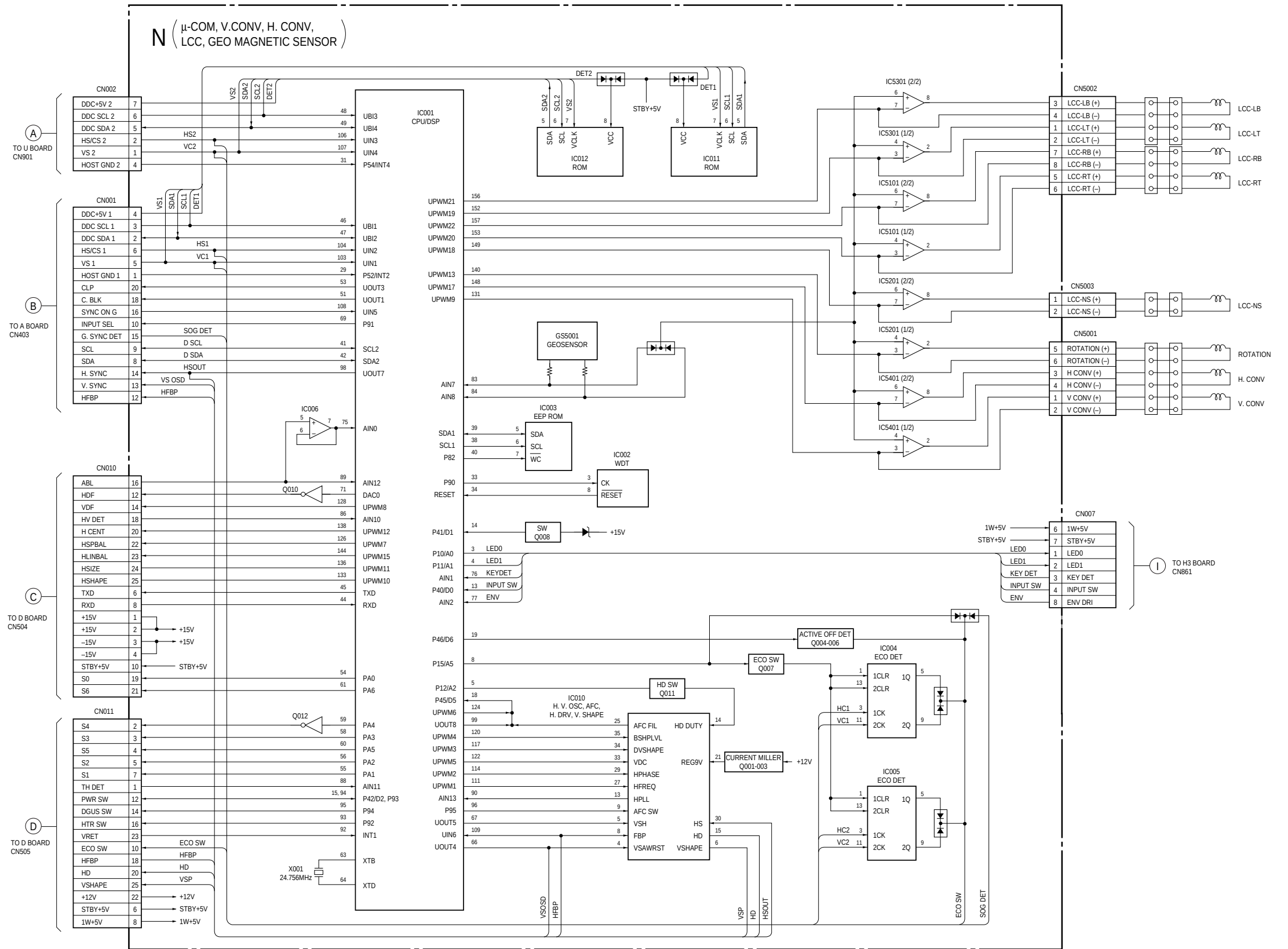
SPECIFICATIONS

CRT	0.24 mm aperture grille pitch 21 inches measured diagonally 90-degree deflection FD Trinitron	Power consumption	Approx. 145 W
Viewable image size	Approx. 403.8 × 302.2 mm (w/h) (15.9 × 11.9 inches) 19.8" viewing image	Dimensions	Approx. 501 × 498 × 498 mm (w/h/d) (19 ³ / ₄ × 19 ⁵ / ₈ × 19 ⁵ / ₈ inches)
Recommended resolution	Horizontal: 1280 dots Vertical: 1024 lines	Mass	Approx. 32 kg (70 lb 9 oz)
Standard image area	Approx. 388 × 291 mm (w/h) (15 ¹ / ₄ × 11 ¹ / ₂ inches) or Approx. 364 × 291 mm (w/h) (14 ³ / ₈ × 11 ¹ / ₂ inches)	Plug and Play	DDC1/2B/2Bi, GTF**
Deflection frequency*	Horizontal: 30 to 121 kHz Vertical: 48 to 160 Hz	* Recommended horizontal and vertical timing condition • Horizontal sync width duty should be more than 4.8% of total horizontal time or 0.8 μs, whichever is larger. • Horizontal blanking width should be more than 2.3 μsec. • Vertical blanking width should be more than 450 μsec. ** If the input signal is Generalized Timing Formula (GTF) compliant, the GTF feature of the monitor will automatically provide an optimal image for the screen.	
AC input voltage/current	100 to 240 V, 50 – 60 Hz, 2.0 – 1.0 A	Design and specifications are subject to change without notice.	

COLOR GRAPHIC DISPLAY

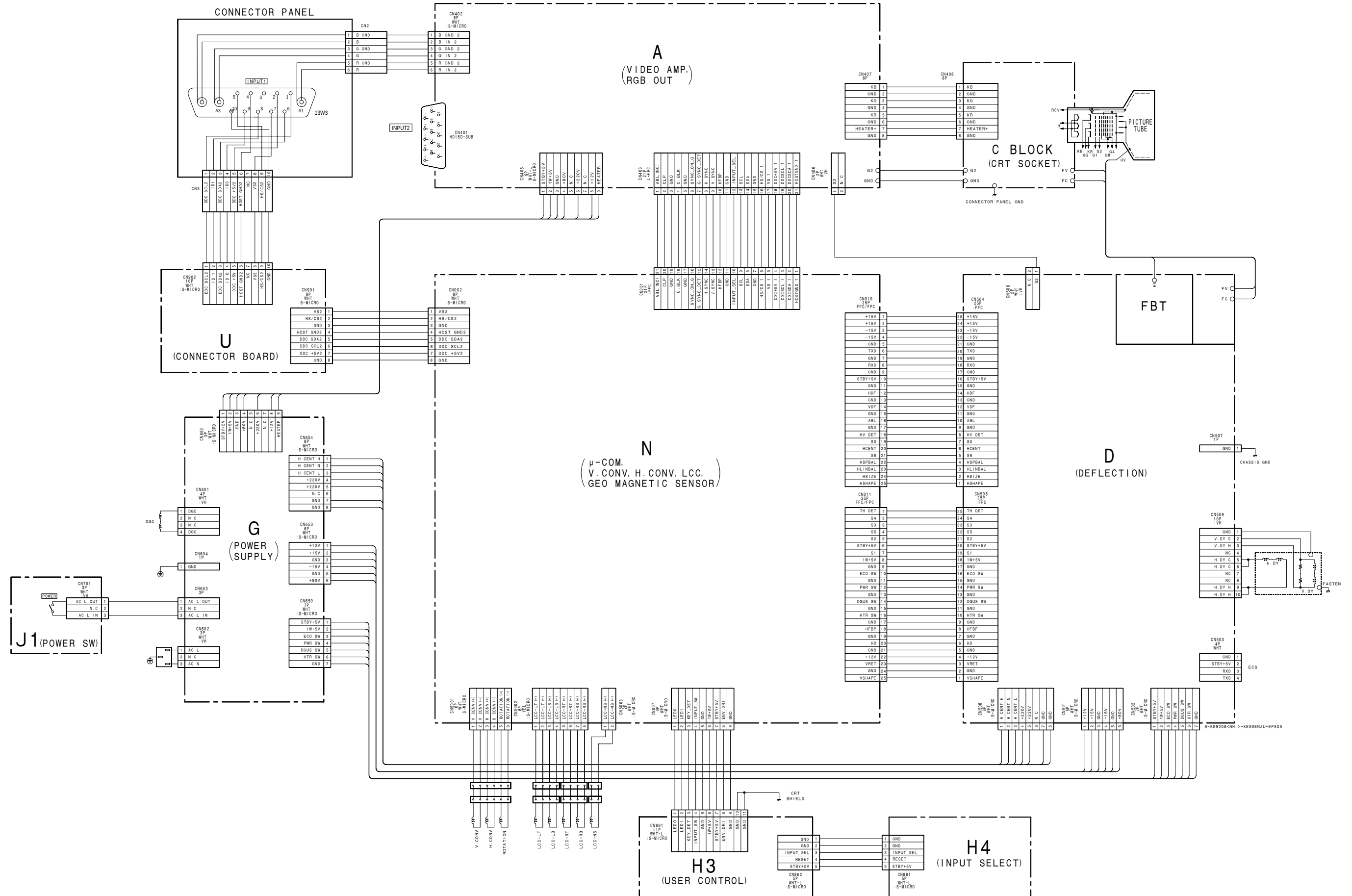
5-1. BLOCK DIAGRAMS



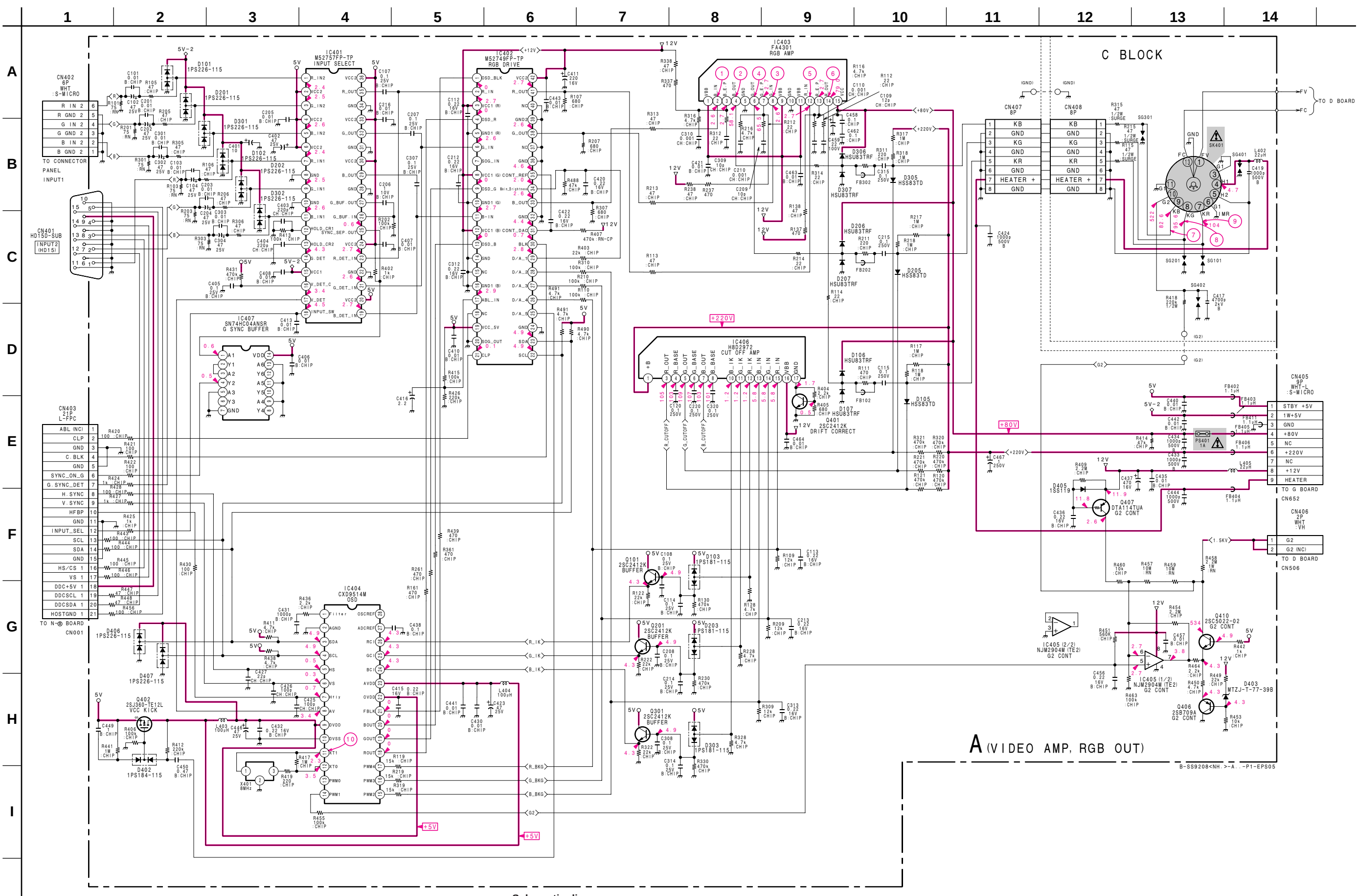


B-SS9208<NH> -BD2-EPS05

5-2. FRAME SCHEMATIC DIAGRAM

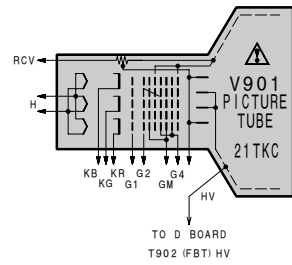
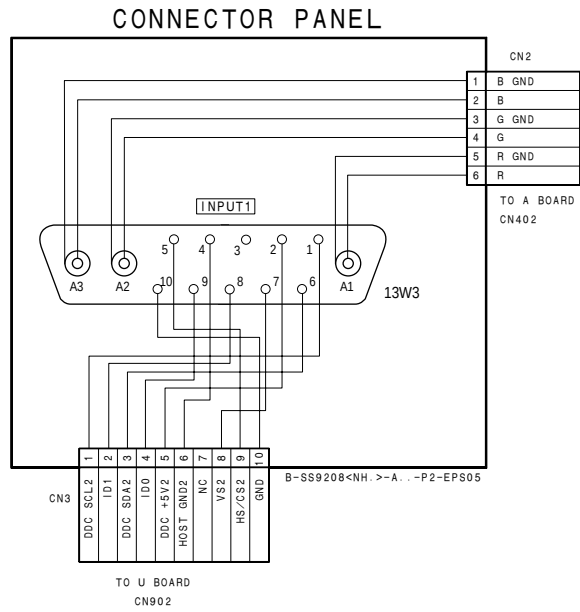


(1) Schematic Diagram of A Board

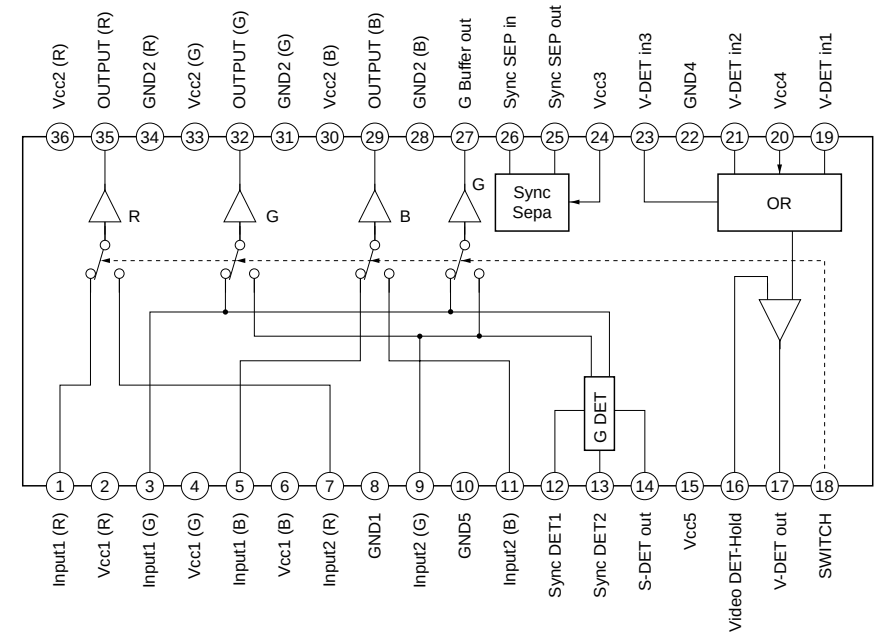


Schematic diagram

A board →

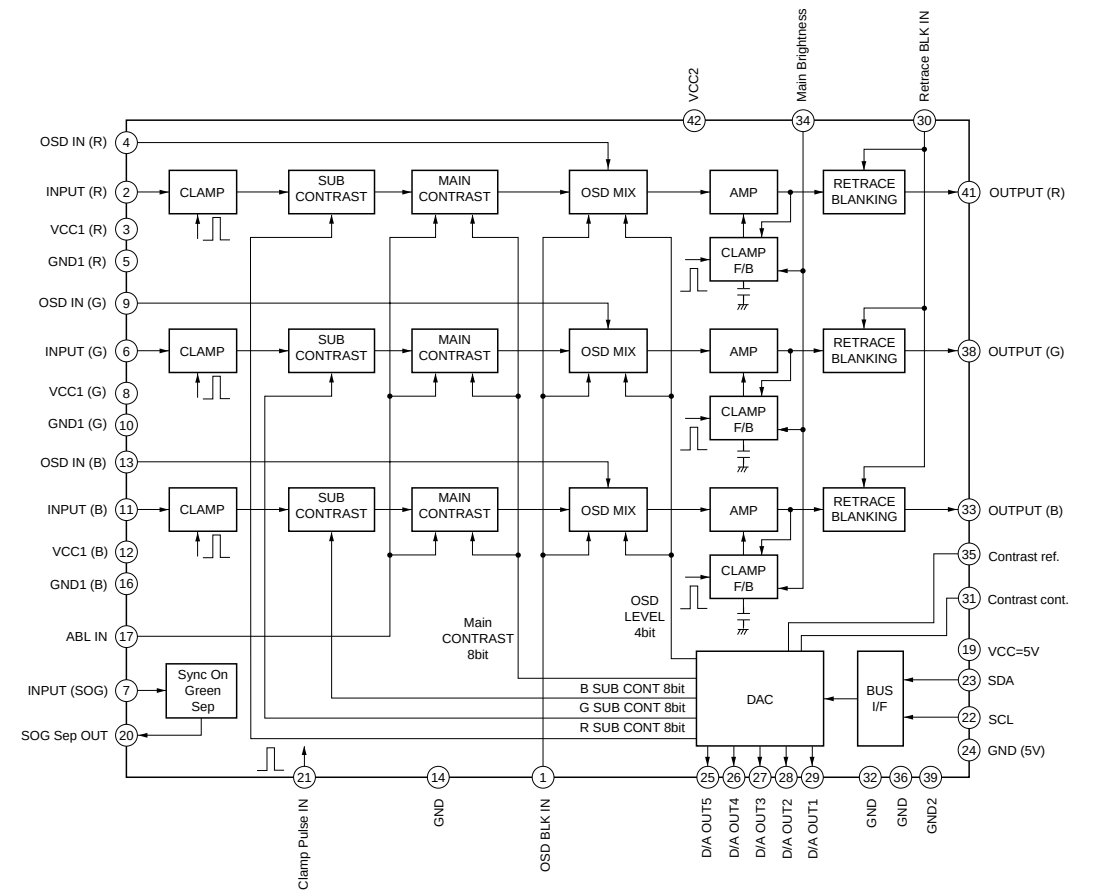
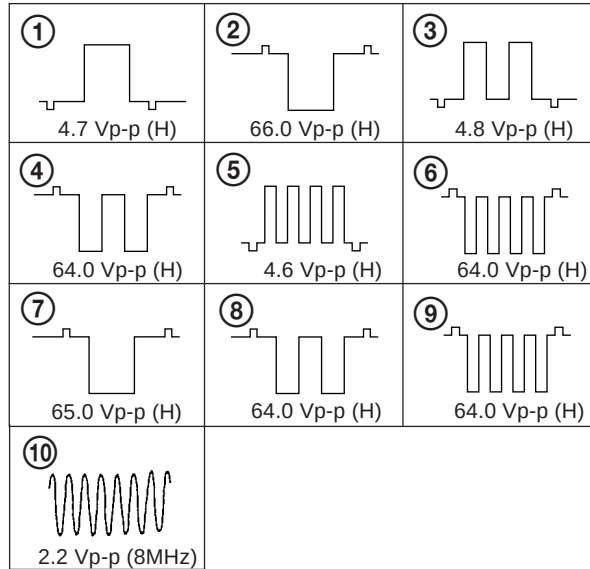


• A BOARD IC401 M52757FP

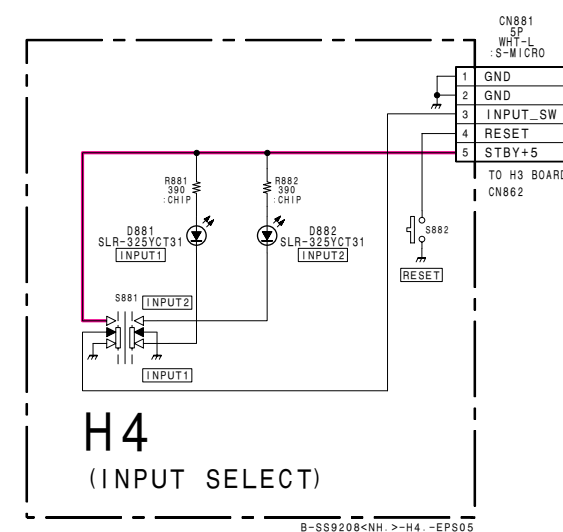


• A BOARD IC402 M52749FP

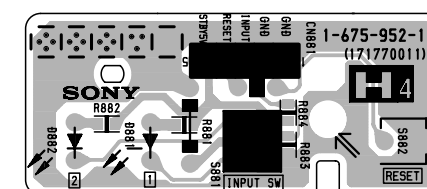
• A BOARD WAVEFORMS



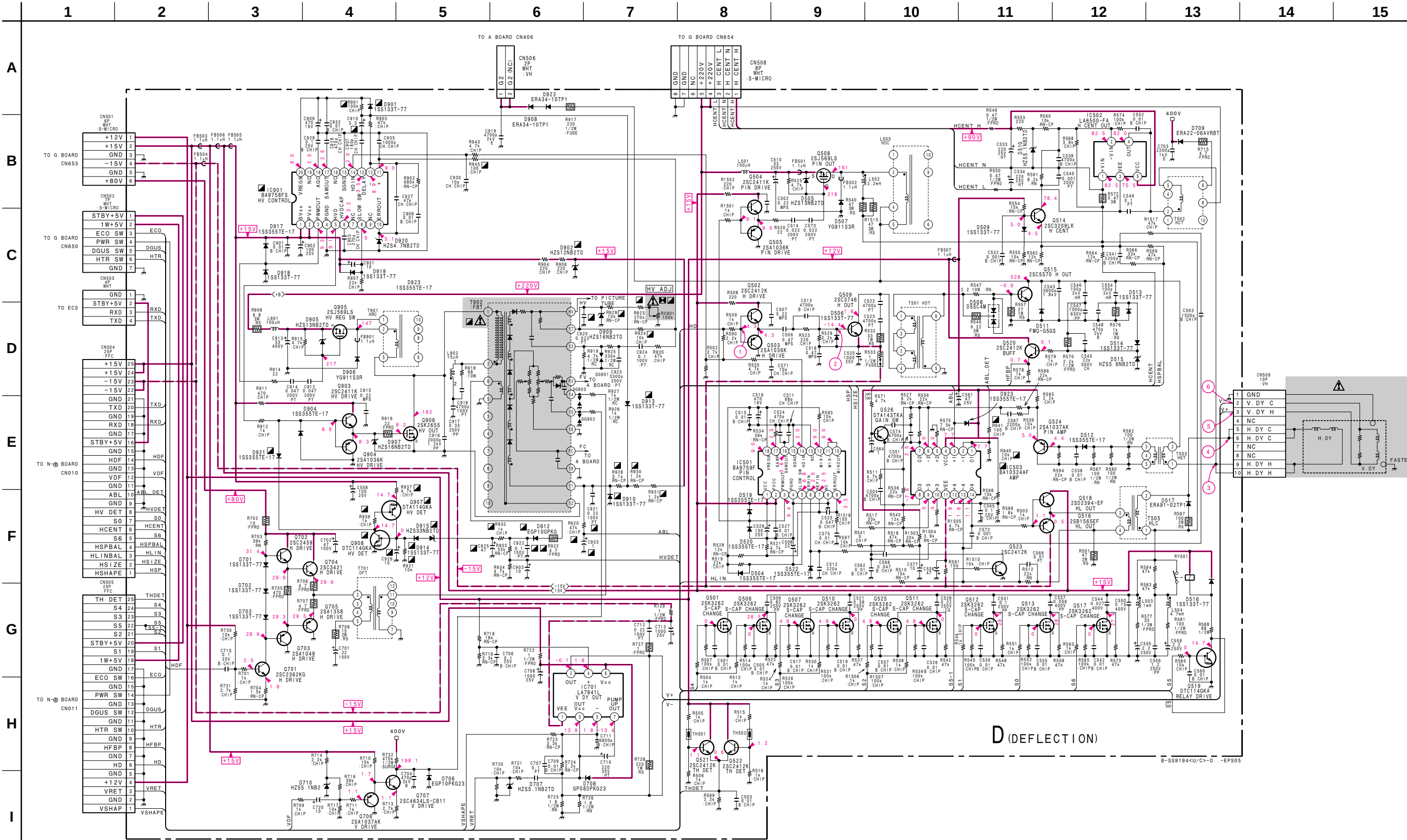
H3 [USER CONTROL] **H4** [INPUT SELECT]



— H4 BOARD —



(3) Schematic Diagram of D Board



Schematic diagrams
← H3 H4 boards

Schematic diagram
D board →

A

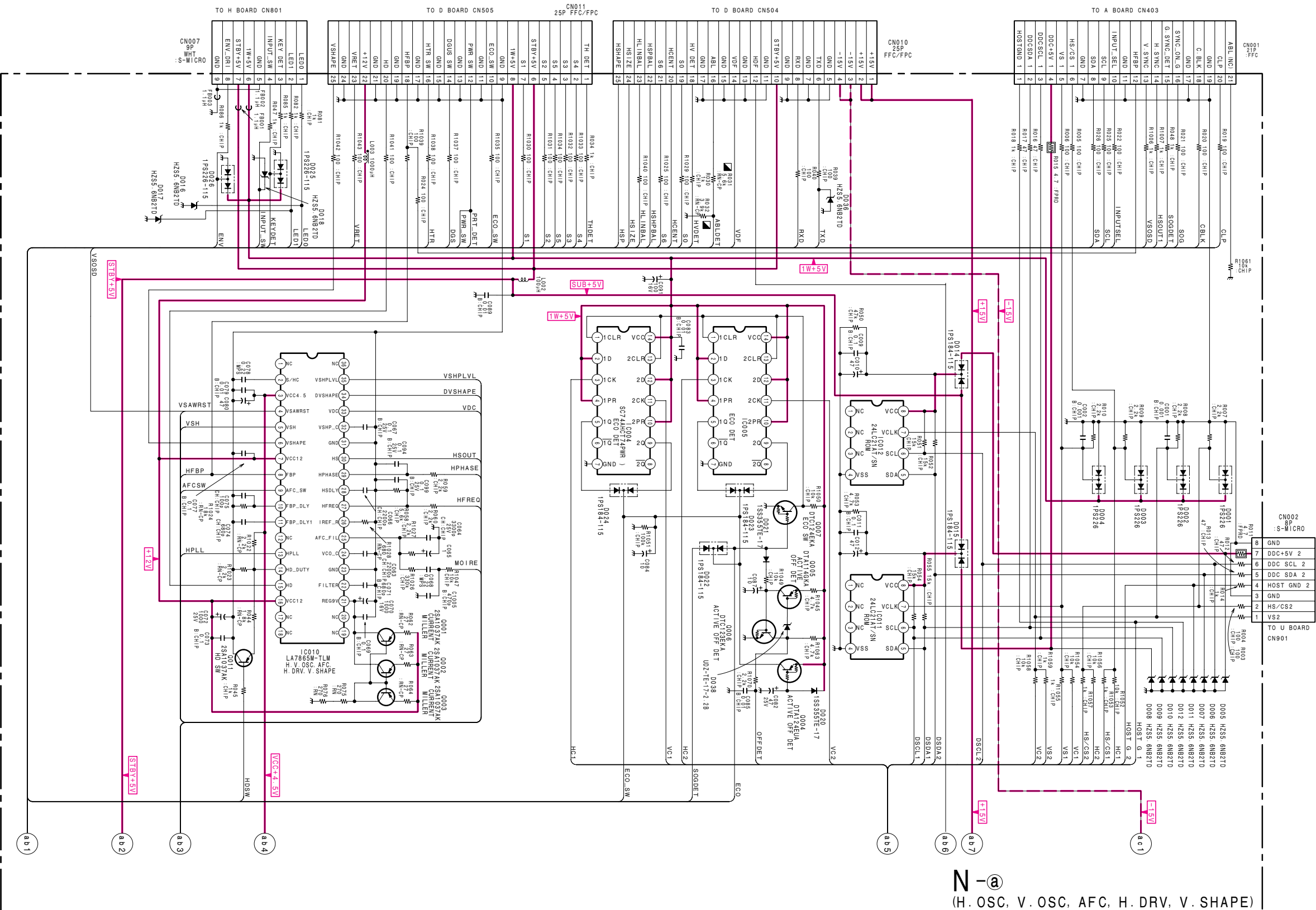


One sheet of N board circuit diagram is divided into three sheets, each having the code N-**(a)** to N-**(c)**. For example, the destination **(ab1)** on the code N-**(a)** sheet is connected to **(ab1)** on the N-**(b)** sheet.

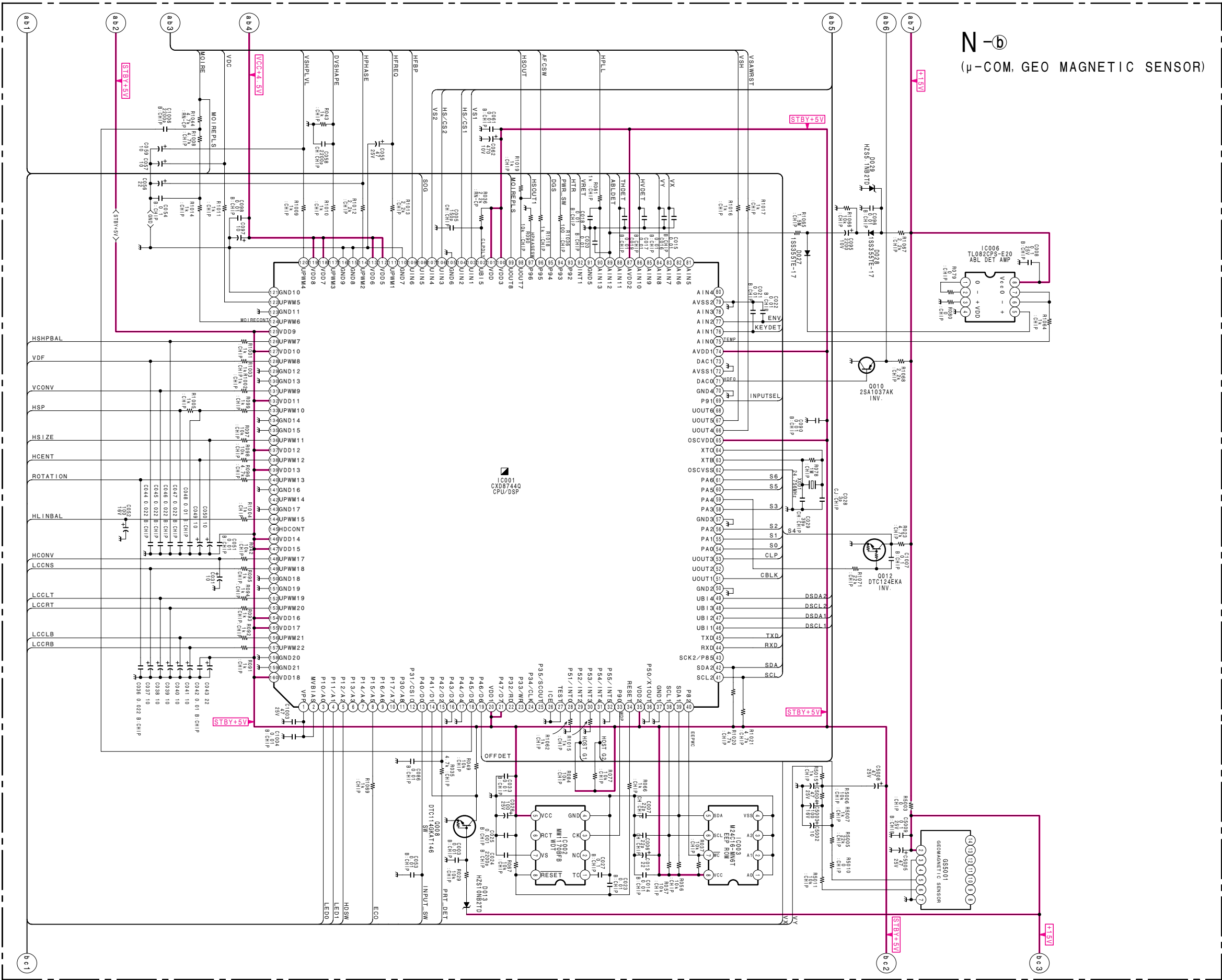
a b 1

Ref. No.

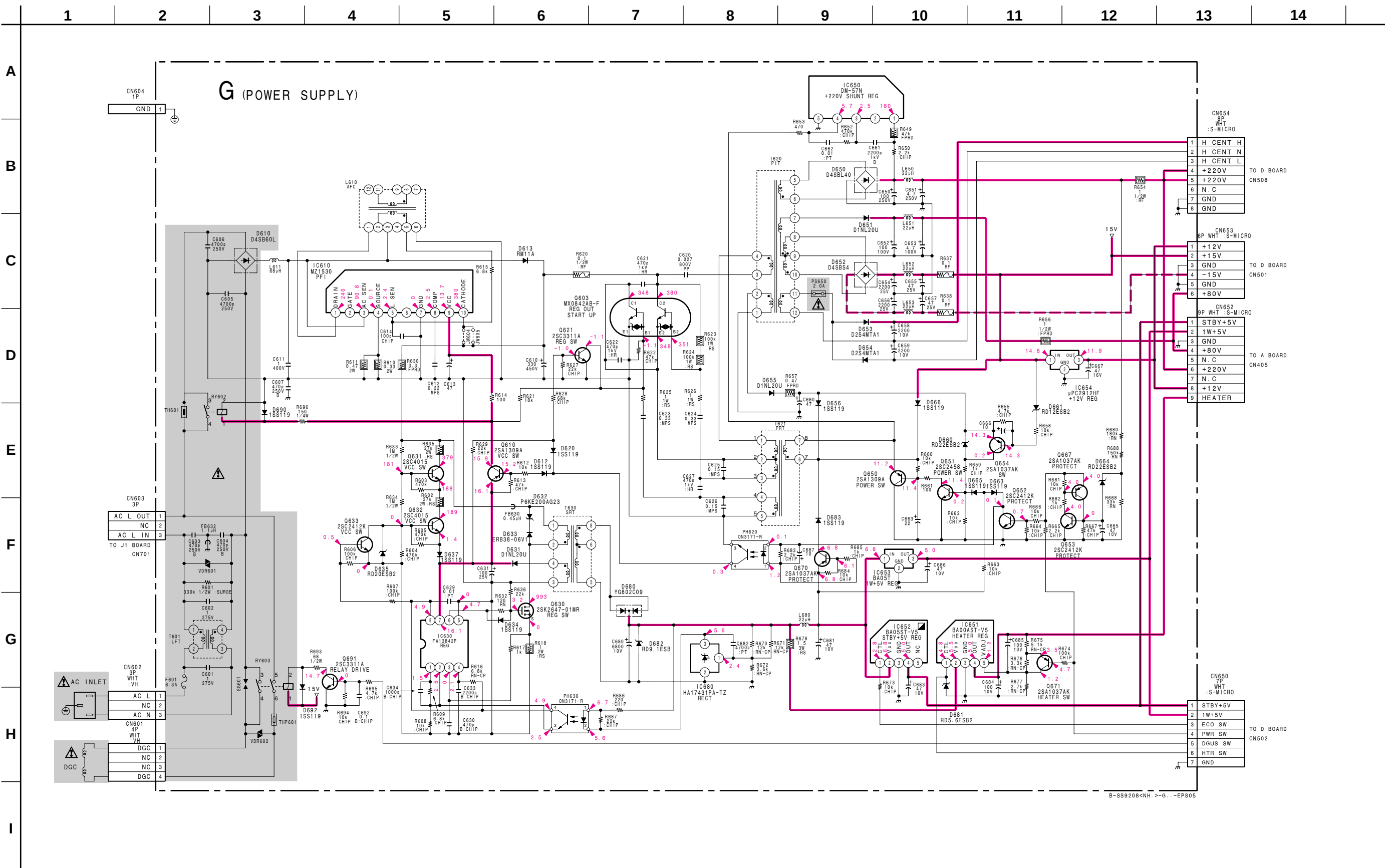
Circuit diagram division code



Schematic diagram
N-(a) board →



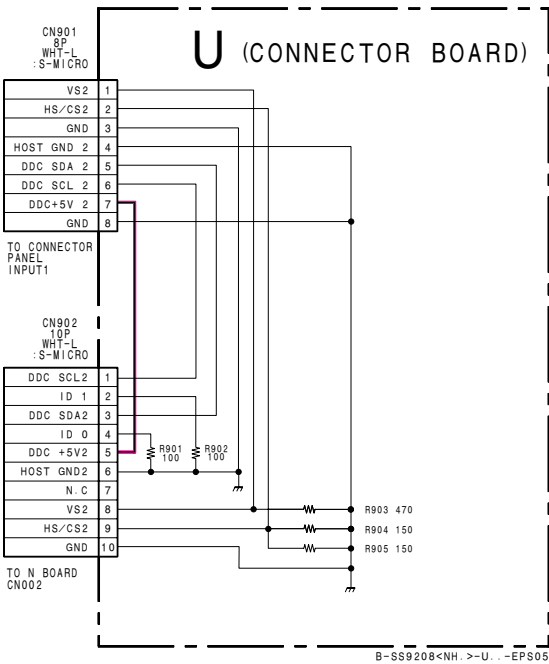
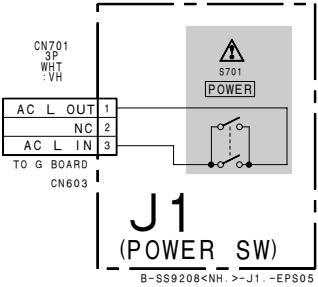
(5) Schematic Diagram of G Board



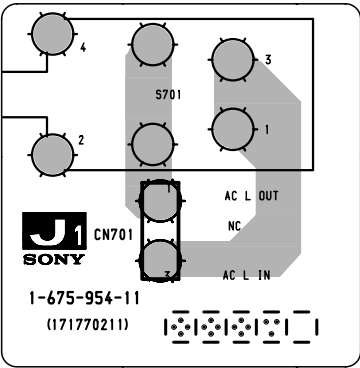
Schematic diagram
G board

(6) Schematic Diagrams of J1, U Boards

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
A														
B														
C														
D														
E														
F														
G														
H														
I														



— J1 BOARD —



— U BOARD —

